

# **MXE-1400 Series**

## MXE-1401

Fanless Embedded Computer

**User's Manual** 



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# Advance Technologies; Automate the World.



# **Revision History**

Revision	Release Date	Description of Change(s)
2.00	July 29, 2016	Initial Release
2.50	Sept. 5, 2016	Section 2.2 Adaptors & Additional Accessories added

# Preface

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Take note of the following conventions used throughout this manual to make sure that users perform certain tasks and instructions properly.



Additional information, aids, and tips that help users perform tasks.



Information to prevent *minor* physical injury, component damage, data loss, and/or program corruption when trying to complete a task.



Information to prevent *serious* physical injury, component damage, data loss, and/or program corruption when trying to complete a specific task.

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# 1 Introduction



### 1.1 Overview

ADLINK's new Matrix MXE-1400 series of rugged quad-core fanless computers, featuring the latest generation of Intel® Atom<sup>™</sup> E3845 (codename: BayTrail) delivers outstanding processor performance with minimum power consumption.

The MXE-1400 series accommodates rich I/O variety in a compact system, with DisplayPort, DVI-I (supporting both DVI and VGA signals), 3 GbE by Intel network interface controllers, 6 USB 2.0, 1 USB 3.0 with dedicated bandwidth, 8 isolated DIO, and 6 COM ports, with 4 BIOS-configurable among RS-232/422 and 485 with auto flow control. Versatile connection to a wide range of applications is provided by dual mini-PCIe slots and USIM socket allowing the MXE-1400 to act as a communications hub for a variety of wireless connections, such as BT/WiFi and 3G/LTE.

Leveraging proprietary mechanical engineering, the MXE-1400 series continues to offer all the popular features of the Matrix E series, including cable-free construction, wide operating temperature ranges, and 5 Grms vibration resistance. The entire ADLINK Matrix line undergoes rigorous testing for operational integrity.

In addition, the MXE-1400 accommodates the most recent modular design available in the Matrix series, keeping essential interfaces on the Mainboard, assigning flexible I/O expandability to the



AFM (Adaptive Function Modules), whereby selection or customization of I/O interfaces based on application requirements.

Combing superior processor performance, wireless capability, and rich I/O with flexible expandability, all in a compact and robust package, the ADLINK MXE-1400 is an ideal choice for a wide range of applications including intelligent transportation, in-vehicle multimedia, surveillance, and factory automation.

## 1.2 Features

- ▶ Quad-core Intel® Atom™ E3845
- ► Single side I/O access with SATA drive bay
- Compact 210(W) x 170(D) x 70(H) mm size
- ► Flexible modular design for I/O expansion
- ► Built-in ADLINK SEMA management solution
- Rugged construction provides fanless -40°C to 70°C operability (w/industrial SSD)
- ► Rich I/O :
  - DVI-I+DisplayPort, 6x USB 2.0, 1x USB 3.0, 3x GbE ports, 6x COM ports, 8x isolated DI/O
  - > 1x SATA-II (3.0 Gb/s), 2x mPCIe slots



Industrial SSD option guarantees cold system boot at -40°C and operation with 100% loading at 70°C when installed.

## 1.3 Specifications

MXE-1401			
System Core			
Processor	Intel® Atom™ E3845		
Chipset	SoC design		
Video	2 independent displays out of 3 channels 1x VGA+ DVI by DVI-I connector 1x Display ports supports up to 2560 x 1600		
Memory	2 GB DDR3L 1066 MHz SODIMM module (Up to 8 GB support)		
I/O Interface			
Ethernet	3 GbE ports (3x Intel® I211 AT)		
Serial Ports 4 BIOS configurable RS-232/422/485 (CON COM4) 2 RS-232 (COM5 & COM6)			
USB	6 USB 2.0 ports + 1 USB 3.0 port		
DIO	8 DIO w/ 1.5KV isolation		
Audio	1 mic-in and 1 line-out		
Mini PCIe	2 internal PCIe mini card sockets		
USIM 1 USIM socket for 3G communication (use 3G-mini module )			
WDT	Supports watchdog timer		
Power Supply			
DC Input	Built-in 9-32 VDC wide-range DC input 3P pluggable connectors with latch (GND, V-, V+)		
AC Input	Optional 80 W external AC-DC adapter for AC Input		
Storage			
SATA HDD 1 onboard SATA-II port for 2.5" HDD/SSD Installation with drive bay			
CompactFlash	1 CFAST slot, supporting PI/O and DMA modes		
Mechanical			
Dimensions	210(W) x 170(D) x 70(H) mm		
Weight	2.0 kg (4.42 lbs)		



MXE-1401				
Mounting	Wall-mount kit			
Environmental				
Operating Temperature	Standard: 0°C to 50°C (w/HDD) Extended Temperature: -40°C to 70°C (w/industrial SSD or CFAST)			
Storage Temperature	-40°C to 85°C (excl. HDD/SDD/CFAST)			
Humidity	Approx. 95% @ 40°C (non-condensing)			
Vibration	Operating, 5 Grms, 5-500 Hz, 3 axes (w/ CFAST or SSD)			
	Operating, 0.5 Grms, 5-500 Hz, 3 axes (w/ HDD)			
ESD	Contact +/-4 KV and Air +/-8 KV			
Shock	Operating, 50 G, half sine 11 ms duration (w/ CFAST or SSD)			
EMC	CE and FCC Class A			

Power Consumption			
Power off 5.76W		In shutdown mode with DC input and only USB keyboard/mouse	
System Idle	13.92W	Under Windows Desktop with no application programs executed	
Processor full load	19.92W	Under Windows with 100% CPU utilization and 2D/3D graphics load	
System full load	43.2W	Under Windows with 100% CPU utilization and simultaneous access to all I/O devices.	
Recommended power supply	80W	With consideration of voltage de-rating under high environmental temperature.	



Figure 1-1: MXE-1400 Functional Block Diagram

## 1.4 Unpacking Checklist

Before unpacking, check the shipping carton for any damage. If the shipping carton and/or contents are damaged, inform your dealer immediately. Retain the shipping carton and packing materials for inspection. Obtain authorization from your dealer before returning any product to ADLINK. Ensure that the following items are included in the package.

- MXE-1401 controller
- Wall-mount bracket (in Accessory Box)
- Screw pack for wall-mounting and HDD fixing (in Accessory Box)
- Quick Start Guide

## 1.5 Mechanical Drawings

Shown with wall-mount brackets installed. For more information, see "Wall-mounting the MXE-1400" on page 31.





All dimensions shown are in millimeters (mm) unless otherwise stated.



Figure 1-2: Top View



Figure 1-3: Front View



Figure 1-4: Rear View



Figure 1-5: (Left) Side View



## 1.6 Front Panel I/O Connectors



Figure 1-6: Front Panel I/O

Α	Power
В	Reset
С	Power header
D	LED indicators
Ε	Audio
F	UART
G	DisplayPort
Н	DVI-I
Ι	I211Gigabit Ethernet
J	USB 3.0 (1600mA supported)
Κ	USB 2.0
L	DC power supply
М	DB-26P digital I/O

Table 1-1: Front Panel I/O Legend

#### 1.6.1 Power Button

Non-latched push button with a blue LED indicator. System is turned on when button is pressed, and the power LED lit. If the system hangs, depressing the button for 5 seconds powers down the system.

### 1.6.2 Power Button Header

Allows connection of external power control cable.

### 1.6.3 Reset Button

Executes hard reset for the MXE-1400.

#### 1.6.4 LED Indicators

In addition to the LED of the power button, LEDs on the front panel indicate the following operations.

LED	Color	Description		
Diagnostia	Green	Lit	No physical storage is connected	
(DG)		Blinking	No memory is installed on either SO-DIMM socket.	
Watchdog (WD)	Yellow	Flashes when watchdog timer starts		
Hard disk drive (HD)	Red	Blinking indicates SATA HDD is active		
User-defined (U1, U2, U3)	White	As set in GPIO		

Table 1-2: LED Indicator Legend



### 1.6.5 DisplayPort Connector

Provides connection to VGA, DVI, HDMI monitor via DisplayPort 1.2 to VGA adapter cable, to DVI adapter cable, and to HDMI adapter cable.



Pin	Signal	Pin	Signal
1	CN_DDPx0+	11	GND
2	GND	12	CN_DDPx3-
3	CN_DDPx0-	13	CN_DDPx_AUX_SEL
4	CN_DDPx1+	14	CN_DDPx_CONFIG2
5	GND	15	CN_DDPx_AUX+
6	CN_DDPx1-	16	GND
7	CN_DDPx2+	17	CN_DDPx_AUX-
8	GND	18	CN_DDPx_HPD
9	CN_DDPx2-	19	GND
10	CN_DDPx3+	20	+V3.3_DDPx_PWR

#### Table 1-3: DisplayPort Pin Assignment

### **Multi-Display Option**

With the computing and graphic performance enhancements of Intel HD Graphics Engine, the MXE-1400 supports two independent displays, configured as follows.

Display 1	DP	DP	VGA by DP
Display 2	DVI	VGA	DVI
Max. Res	2560 x 1600	2560 x 1600	2048 x 1280
Display 1	@60Hz	@60Hz	@60Hz
Max. Res	1920 x 1200	2048 x 1280	1920 x 1200
Display 2	@60Hz	@60Hz	@60Hz

Table 1-4: Multi-Display Configuration

### **Optional Accessories for DP to VGA/DVI Adapters**

Accessory	P/N
DisplayPort to DVI Active cable	30-01157-0000
DisplayPort to VGA cable	30-01121-0000
DisplayPort to DVI cable	30-01120-0000
DisplayPort to HDMI cable	30-01119-0000

### 1.6.6 Audio Connector

Provides connection of Mic and Headphone/Speaker channels, as marked.



### 1.6.7 DVI-I Connector

For connection to external monitor, which can separate into individual VGA and DVI-D (single link) interfaces.

1	2	3	4	5	6	7	8	
9	10	11	12	13	14	15	16	
17	18	19	20	21	22	23	24	

Pin	Signal	Pin	Signal	Pin	Signal
1	DVI_Data2-	11	GND	21	N/C
2	DVI_Data2+	12	N/C	22	GND
3	GND	13	N/C	23	DVI clock+
4	CRT DDC clock	14	+5V	24	DVI clock-
5	CRT DDC data	15	GND		
6	DVI DDC clock	16	Hot plug detect	C1	Analog Red
7	DVI DDC data	17	DVI_Data0-	C2	Analog Green
8	Analog vertical sync	18	DVI_Data0+	C3	Analog Blue
9	DVI_Data1-	19	GND	C4	Analog / horizontal sync
10	DVI_Data1+	20	N/C	C5	Analog GND

#### Table 1-5: DVI-I Connector Pin Assignment

### 1.6.8 Gigabit Ethernet Ports

3 Gigabit Ethernet ports are based on the Intel i211-AT GbE controller, featuring:

- ▶ IEEE 802.3az Energy Efficient Ethernet
- ▶ IEEE 1588/802.1AS precision time synchronization
- ▶ IEEE 802.3Qav traffic shaper
- ► Interrupt moderation, VLAN support, IP checksum offload
- PCIe OBFF (optimized buffer flush/fill) improves system power management
- ► Four transmit and four receive queues
- RSS and MSI-X to lower CPU utilization in multicore systems
- ► ECC (error correcting memory) in packet buffers
- ▶ Wake-On-LAN support
- NC-SI increases bandwidth passthrough
- Preboot eXecution Environment (PXE) flash interface support
- ▶ Jumbo frame support



Figure 1-7: Gigabit Ethernet Ports



LED	Color	Status	Description
		Unlit	Ethernet port is disconnected.
Active/Link	Yellow	Lit	Ethernet port is connected with no activity.
		Flashing	Ethernet port is connected and active.
	Quant	Unlit	10Mbps
Speed	Green/ Orange	Green	100 Mbps
	Crange	Orange	1000 Mbps

#### Table 1-6: Gigabit Ethernet Port LED Function

#### 1.6.9 COM Port Connectors

Of 6 COM ports through D-sub 9-pin connectors, COM1 to COM4 support RS-232/422/485 modes via BIOS setting, and COM5 and COM6 support only RS-232.



Figure 1-8: COM Port Connector

Pin	Signal				
	RS232	RS422	RS485		
1	DCD#	TXD422-	485DATA-		

Pin	Signal				
2	RXD	TXD422+	485DATA+		
3	TXD	RXD422+	N/S		
4	DTR#	RXD422-	N/S		
5	GND	N/S	N/S		
6	DSR#	N/S	N/S		
7	RTS#	N/S	N/S		
8	CTS#	N/S	N/S		
9	RI#	N/S	N/S		

Table 1-7: COM Port Pin Assignments

#### 1.6.10 USB 3.0 Ports

USB 3.0 supporting Type A connection is compatible with Super-Speed, Hi-Speed, full-speed and low-speed USB devices, with support for multiple boot devices, including USB flash, USB external HDD, and USB CD-ROM drivers and boot priority and boot device configured in BIOS.

NOTE:	When using USB CD-ROM via USB 3.0 port to re-install or repair the OS, cold boot should be utilized USB 3.0 port supports additional current up to 1600mA, positioned as shown in Table 1-1 on page 8

#### 1.6.11 CFast Port

A type II push-push CFast host connector on the front panel connects to the host controller by SATA interface, with data transfer rates up to 3.0Gb/s(300MB/s)/1.5Gb/s(150MB/s) supported.

#### 1.6.12 USB 2.0 Ports

6 Type A USB 2.0 ports support multiple boot devices, including USB flash, USB external HDD, and USB CD-ROM drivers, with Boot Priority and Device configured via BIOS.



### 1.6.13 DC Power Connector

The DC power supply connector consists of V+, chassis ground, and V- pins (from right to left), with V+ and V- pins accepting DC power input and chassis ground pin enhancing EMC compatibility. The DC power input accepts input from 9V DC to 32 VDC.

Pin	Signal
1	V+(DC_IN)
2	GND(CHGND)
3	V- (DGND)

Table 1-8: DC Power Supply Connector Signals

#### 1.6.14 DB-26P Digital I/O Connector

8CH isolated digital input and 8CH isolated digital output are provided through the DB-26P connector. General purpose input/output application with isolation requirement is recommended.



Figure 1-9: DB-26P Digital I/O Connector

Pin	Signal Name	Pin	Signal Name
1	IDI_0L	14	IDI_4H
2	IDI_1L	15	IDI_5H
3	IDI_2L	16	IDI_6H

Pin	Signal Name	Pin	Signal Name
4	IDI_3L	17	IDI_7H
5	IDI_45L	18	EOGND
6	EOGND	19	IDO_0
7	IDI_67L	20	IDO_1
8	+VDD	21	IDO_2
9	+5DIO_CN_ISO	22	IDO_3
10	IDI_0H	23	IDO_4
11	IDI_1H	24	IDO_5
12	IDI_2H	25	IDO_6
13	IDI_3H	26	IDO_7

Table 1-9: DB-26P Connector Pin Assignment

8CH Isolated DI	8CH Isolated DO
Logic high: 5 to 24V	Output: Open Drain N- Channel Power MOSFET driver
Logic low: 0 to 1.5V	250mA for all channels @ 60°C, 100% duty
Input resistance: 2.4kΩ @ 0.5W	Supply voltage: 5 to 35 VDC
Interrupt source: DI	Isolation voltage: 1.5kVDC channel 0 to 7
Isolation voltage: 1.5kV DC channel 0 to 7	200mA for +V5DIO_ISO (max.)

Table 1-10: Digital I/O Specifications



### Optional Accessories for DB-26P Digital I/O Connector

Accessory	P/N
DIN-37D-01 Terminal board with one 37-pin D- sub connector and DIN-rail mounting	91-14025-1020
DIO-to-DIN37 cable (26-pin to 37-pin)	30-01143-0000

### **Isolated Digital Input Circuits**

The input can accept voltages up to 24V, with 2.4k $\Omega$  input resistors. Connections between outside signals are as follows.



Figure 1-10: Isolated/Differential Digital Input Circuit

### **Isolated Digital Output Circuits**

Each isolation digital output channel adopts a MOSFET transistor, capable of driving peak current up to 250mA (sustained current up to 100 mA) with voltage ranges from 5V to 35V.The VDD pin is connected in serial with a flywheel diode to protect the driver during inductance loading, such as relay, motor, or solenoid. The VDD must connect to external power to form a flywheel current loop.



Figure 1-11: Isolated Digital Output Circuits



Figure 1-12: Isolated Digital Output Sample Application Circuit



## 1.7 Rear Panel



Figure 1-13: Rear Panel I/O

Α	Antenna connection plugs (X4)	
В	CFast Slot	
С	HDD Fast Plug Slot	



#### 1.7.1 CFast Slot

The Type II push-push CFast host connector on the back panel connects to the host controller by SATA interface, supporting data transfer up to 3.0Gb/s (300MB/s)/1.5Gb/s (150MB/s).

### 1.7.2 HDD Fast Plug Slot

The Fast Plug host connector connecting to the host controller by SATA interface supports data transfer up to 3.0Gb/s (300MB/s)/ 1.5Gb/s (150MB/s).

## 1.8 Internal I/O Connectors



Mainboard is shown in default configuration, with stacked AFM board



Figure 1-14: Internal I/O

А	Extended Reset/LED wafer
В	GPS module power header – 5V
С	GPS module power header – 3.3V
D	USB 2.0 connector
E	Clear CMOS jumper
F	USIM slot
G	Mini-PCIE slot
Н	SATA Connector

#### Table 1-12: Internal I/O Legend



### 1.8.1 Clearing CMOS Jumper

Under conditions in which the MXE-1400 fails to boot, clearing the BIOS content stored in CMOS and restoring the default settings may be effective. To clear CMOS, short Pin #1 and Pin #2 of JP1 and remove the jumper, after which the CMOS will be restored to factory default settings.



Figure 1-15: Clear CMOS Jumper Pin Settings

Pin	Description
1	RTCRST#
2	Gnd

 Table 1-13: Clear CMOS Jumper Pin Assignment

### 1.8.2 DC 5V and 3.3V Connectors for GPS Module

The two power connectors, for GPS module use, carry a maximum current rating of 1A each.



Figure 1-16: DC 5V and 3.3V Connectors Configuration

Pin	Description
CN9	
1	+5V
2	Gnd
CN10	
1	+3.3V
2	Gnd

#### 1.8.3 USIM Port

Use of 3.5G mini-PCIe module requires a SIM card for communication with a telecom operator. The MXE-1400 provides a USIM port connected to the mini-PCIe connector, with which a SIM card and 3.5G mini-PCIe module can be installed to facilitate 3.5G communication.

### 1.8.4 SATA Connectors

The SATA connectors support data transfer up to 3.0Gb/s (300MB/ s), with a SATA host controller supporting legacy mode using I/O space and AHCI mode using memory space. The SATA connector is compatible with a 2.5in. hard disk drive (HDD) or solid



state disk (SSD), requiring installation into the SATA connector with a HDD bracket.

#### 1.8.5 Extended Power/Reset/LED

Internal LED connectors power indicators for the Power (from CN27) and Reset buttons (CN31), assigned as shown.



Figure 1-17: Extended Reset/LED Configuration

Pin	Description	
CN27		
1	+V3.3SB	
2	Power LED	
CN31		
1	Reset Button	
2	Gnd	

Table 1-15: Extended Reset/LED Pin Assignments
# 2 Getting Started

This chapter discusses installation of hard disk drive, mini-PCIe module, and CFast card. In addition to connection and use of DIO and COM ports, wall-mount installation is also described.

# 2.1 Unpacking Checklist

Before unpacking, check the shipping carton for any damage. If the shipping carton and/or contents are damaged, inform your dealer immediately. Retain the shipping carton and packing materials for inspection. Obtain authorization from your dealer before returning any product to ADLINK. Ensure that the following items are included in the package.

- MXE-1400 controller
- Wall-mount bracket (in Accessory Box)
- Screw pack for wall-mounting and HDD fixing (in Accessory Box)
- Quick Start Guide

# 2.2 Adaptors & Additional Accessories

Device adaptors and other optional accessories should only be obtained through your ADLINK dealer. For more information, see "Getting Service" on page 87.

# 2.3 Installing a Hard Disk Drive

Before installing hard disk drives, the HDD bracket must first be removed.



1. Remove thumbscrews fixing the HDD hatch.



2. Withdraw the HDD bracket.





3. Place the HDD or SSD into the bracket.

4. Use the 4 provided M3 screws to fix the unit to the bracket.



- 5. Gently slide the bracket back into the bay until the SATA connector on the PCB is engaged.
- 6. Refasten the thumbscrews.



# 2.4 Installing a Mini-PCIe Device

Remove the bottom cover before installing.

1. Remove 6 bottom screws.



2. Remove 4 side screws (2 from each side).





4. Insert the mini-PCI-E wireless module into the slot at an angle.



5. Gently seat the mini-PCI-E wireless module and fix with the 4 M2.5-P-head-L5 screws.

3. Lift the bottom cover



6. Replace the cover and refasten the 10 screws.

# 2.5 Installing CFast Card

The MXE-1400 provides an external CFast socket to accommodate one CFast card for additional storage.

1. Remove the 2 screws and the CFast cover from the rear panel.



2. Gently insert the CFast card into the CFast socket.



3. Replace the CFast cover and refasten the 2 screws.

# 2.6 Connecting DC power



Before introducing DC power to the MXE-1400, ensure the voltage and polarity provided are compatible with the DC input. Improper input voltage and/or polarity can be responsible for system damage.

The DC power input connector of the MXE-1400 utilizes V+, V- , and chassis ground pins, and accepts input voltage as shown previously.

- 1. Connect DC power.
- 2. Fix the DC connector using the 2 screws.



# 2.7 Wall-mounting the MXE-1400

The MXE-1400 is shipped with wall-mount brackets and accessory screws, with mounting procedures as follows.



All dimensions shown are in millimeters (mm) unless otherwise stated.





To sustain structural integrity, the device should not be wallmounted with the rear panel on the bottom.

1. Remove the 4 plastic pads from the underside panel corners.



2. Use the 4 M4 screws shipped with the controller to fix the 2 wall-mount brackets, also included, to the chassis, according to the spacing dimensions of the screw holes and brackets, as shown.









3. Once final assembly as shown is complete, mount the MXE-1400 series controller on the wall via screw holes.



# 2.8 Cooling Considerations

Heat-generating components of the MXE-1400 (such as CPU and PCH) are all situated on the top of the system. These components directly contact the heat sink via thermal pads and dissipate heat generated by the components. To maximize efficiency of heat dissipation, maintain a minimum of 2 inches (5cm) clearance on the top of the MXE-1400.

# 2.9 Driver Installation

Download requisite drivers, as follows, for your system from http:// www.adlinktech.com and install.

- ► Audio
- Chipset
- ► Graphic
- ► LAN
- ▶ USB 3.0



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# Appendix A Watchdog Timer (WDT) & DI/O Function Libraries

This appendix describes use of the watchdog timer (WDT) function library for the MXE-1400.

The watchdog timer is a hardware mechanism provided to reset the system if the operating system or an application stalls. After starting, the watchdog timer in the application must be periodically reset before the timer expires. Once the watchdog timer expires, a hardware-generated signal is sent to reset the system.

DI/O provides input/output to support inter-device communications. Simple programming guides allow easy transmission of digital signals between the system and attached peripherals.

# A.1 WDT with API/Windows

Matrix WDT API library files and a demo program (incl. source code) can be downloaded from http://www.adlinktech.com.

To use the WDT function library for MXE-1400 series, include the header file matrix\_wdt.h and linkage library matrix\_wdt.lib in the C++ project.

# InitWDT

Initializes watchdog timer function of MXE-1400. InitWDT must be called before the invocation of any other WDT function.

@ Syntax

C/C++

BOOL InitWDT()

#### @ Parameters

None

#### @ Return code

**TRUE** if watchdog timer is successfully initialized.

FALSE if watchdog timer fails to initialize.



#### SetWDT

Sets the timeout value of the watchdog timer. There are two parameters for this function to indicate the timeout ticks and unit. ResetWDT or StopWDT should be called before the expiration of watchdog timer, or the system will reset.

#### @ Syntax

C/C++

BOOL SetWDT(BYTE tick, BYTE unit)

#### @ Parameters

#### tick

Specify the number of ticks for watchdog timer. A valid value is 1 - 255.

#### unit

Specify the timeout ticks of the watchdog timer.

Value	Description
0	The unit for one tick is one second. For example, when one tick is specified as 100 and the unit as 0, the timeout value is 100 seconds.
1	The unit for one tick is one minute. For example, whenone tick is specified as 100 and the unit as 1, the timeout value is 100 minutes.

#### @ Return codes

TRUE if timeout value of watchdog timer is successfully set.

FALSE if timeout value of watchdog timer is failed to set.

#### StartWDT

Starts watchdog timer function. Once the StartWDT is invoked, the watchdog timer starts. ResetWDT or StopWDT should be called before the expiration of watchdog timer, or the system will reset.

#### @ Syntax

C/C++

```
BOOL StartWDT()
```

#### @ Parameters

None

#### @ Return codes

**TRUE** if watchdog timer is successfully started.

**FALSE** if watchdog timer is failed to start.

#### ResetWDT

Resets the watchdog timer. The invocation of ResetWDT allows restoration of the watchdog timer to the initial timeout value specified in SetWDT function. ResetWDT or StopWDT should be called before the expiration of the watchdog timer, or the system will reset.

#### @ Syntax

C/C++

BOOL ResetWDT()

#### @ Parameters

None

#### @ Return codes

**TRUE** if watchdog timer is successfully reset.

**FALSE** if watchdog timer fails to reset.

# StopWDT

Stops the watchdog timer.

#### @ Syntax

C/C++

BOOL StopWDT()

#### @ Parameters

None

#### @ Return codes

**TRUE** if watchdog timer is successfully stopped.



FALSE if watchdog timer fails to stop.

# A.2 DI/O with API/Windows

Matrix DI/O API library files and a demo program (incl. source code) are located on the included driver CD or downloaded from http://www.adlinktech.com.

To use the DI/O function library for MXE-1400 series, include the header file matrix\_dio.h and linkage library matrix\_dio.lib in the C++ project.

DI/O functions are as follows.

#### **GPIO\_Init**

Reserves system resources for digital input/output API service. It is necessary to call this function before using other MXE-1400 DI/O functions.

#### @ Syntax

C/C++

I16 GPIO\_Init(void)

#### @ Parameters

None

#### @ Return code

NoError

ErrorOpenDriverFailed

ErrorDeviceIoctl

# GPI\_Read()

Reads the digital logic state of the digital input line..

#### @ Syntax

C/C++

I16 GPI\_Read(U16 \*pwState)

#### @ Parameters

pwState

Returns the digital logic state of MXE-1400 digital input channels 1 to 8 (bit 0 to 7)

#### @ Return code

```
NoError
ErrorOpenDriverFailed
ErrorDeviceIoctl
```

# GPO\_Write()

Sets the digital logic state of the digital output line.

#### @ Syntax

C/C++

I16 GPO\_Write(U16 wState)

#### @ Parameters

State

Sets the digital logic state of MXE-1400 digital output channels 1 to 8 (bit 0 to 7) to 0 or 1.

#### @ Return code

```
NoError
ErrorOpenDriverFailed
ErrorDeviceIoctl
```

# GPO\_Read()

Reads the digital logic state of the digital output line.

#### @ Syntax

C/C++

I16 GPO\_Read(U16 \*pwState)

#### @ Parameters

pwState

Returns the digital logic state of MXE-1400 digital output channels 1 to 8 (bit 0 to 7).

# @ Return code



NoError ErrorOpenDriverFailed ErrorDeviceIoctl

# Appendix B BIOS Setup



BIOS options in the manual are for reference only, and are subject to configuration. Users are welcome to download the latest BIOS version from the ADLINK website.

The Basic Input/Output System (BIOS) is a program that provides a basic level of communication between the processor and peripherals. In addition, the BIOS also contains codes for various advanced features applied to the MXE-1400. The BIOS setup program includes menus for configuring settings and enabling features of the MXE-1400 series. Most users do not need to use the BIOS setup program, as the MXE-1400 ships with default settings that work well for most configurations.



Changing BIOS settings may lead to incorrect controller behavior and possible inability to boot. In such a case, Section 1.8.1 on page 22 provides instruction on clearing the CMOS and restoring default settings



# B.1 Main

Aptio Setup Utility – Main Advanced Chipset Security	Copyright (C) 2013 American Boot Save & Exit	Megatrends, Inc.
BIOS Information BIOS Vendor Core Version BOARD Version System Language System Date System Time Access Level	American Megatrends 5.009 ADLINK MXE1400 0201 A02 [English] [Mon 06/08/2015] [09:99:06] Administrator	Choose the system default language ++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
Version 2.16.1242. Cc	pyright (C) 2013 American M	egatrends, Inc.

# **B.1.1 BIOS Information**

Shows current system BIOS core version, BIOS version and Board version.

#### B.1.2 System Time/System Date

Changes system time and date. Highlight System Time or System Date using the up or down <Arrow> keys. Enter new values using the keyboard then <Enter>. Use < Tab > to move between fields. The date must be entered in MM/DD/YY format. The time is entered in HH:MM:SS format.



The time is in 24-hour format, for example, 5:30 A.M. appears as 05:30:00, and 5:30 P.M. as 17:30:00.

# B.2 Advanced

Aptio Setup Utility – Copyright (C) 2013 American Main <mark>Advanced</mark> Chipset Security Boot Save & Exit	Megatrends, Inc.
<ul> <li>CPU Configuration</li> <li>Memory Configuration</li> <li>Onboard devices configuration</li> <li>Advanced Power Management</li> <li>SATA Configuration</li> <li>Serial Port Console Redirection</li> <li>CSM Configuration</li> <li>System Management (SEMA)</li> </ul>	CPU Configuration Parameters ++: Select Screen 11: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit



Setting incorrect or conflicting values in Advanced BIOS Setup may cause system malfunction.



# **CPU Configuration**

CPU Signature Microcode Patch CPU Speed Max CPU Speed Min CPU Speed Processor Cores Intel HT Technology Intel VT-x Technology 64-bit	30679 901 1918 MHz 1918 MHz 500 MHz 4 Not Supported Supported Supported	Disabled for Windows XP
L1 Data Cache L1 Code Cache L2 Cache L3 Cache	24 kB x 4 32 kB x 4 1024 kB x 2 Not Present	++: Select Screen †J: Select Item Entre: Select
Limit CPUID Maximum Execute Disable Bit Hardware Prefetcher Adjacent Cache Line Prefetch Intel Virtualization Technology EIST CPU C state Report CPU C tate Report Max CPU C-state	[Disabled] [Enabled] [Enabled] [Enabled] [Enabled] [Enabled] [Enabled] [Enabled] [Conj	<pre>+/-: Schange Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save &amp; Exit ESC: Exit</pre>

#### Hyper-Threading

Enabled for OS optimized for Hyper-Threading Technology and disabled for those not optimized. When disabled only one thread per enabled core is enabled

#### **Active Processor Cores**

Number of cores to enable in each processor package.

#### Limit CPUID Maximum

Limits the CPUID return value, should be disabled in older OS to avoid system error.

#### **Execute Disable Bit**

Can prevent certain classes of malicious buffer overflow.

#### Intel Virtualization Technology

When enabled, a VMM can utilize the additional hardware capabilities provided by Vanderpool Technology

#### Hardware Prefetcher

Enables/disables the Mid Level Cache (L2) streamer prefetcher

#### Adjacent Cache Line Prefetch

Enables/disables prefetching of adjacent cache lines.

#### CPU AES

Enables/Disables CPU Advanced Encryption Standard instructions.

#### EIST

Enables/disables Intel SpeedStep Technology.

#### **Turbo Mode**

Enables/disables Intel TurboBoost Technology.

#### **Energy Performance**

Optimizes performance/power saving ratio.

#### CPU C state

Enables/disables CPU C states

#### Package C State limit

CPU Package C State limit.

#### **ACPI CTDP BIOS**

Enables/Disables ACPI CTDP BIOS support.

#### Configurable TDP Level

Allows reconfiguration of TDP levels based on current power and thermal delivery capabilities of the system.

#### Config TDP Lock

Locks the Config TDP Control register.

#### **TCC Activation Offset**

Offset from the factory TCC activation temperature

#### Intel TXT (LT) Support

Enables/Disables Intel<sup>®</sup> TXT(LT) support.



# **Memory Configuration**

Aptio Setup Uti Advanced	lity – Copyright (C) 2013 Amer.	ican Megatrends, Inc.
Memory Configuration		
Total Memory	4096 MB (LPDDR3)	
Memory SlotO Memory Slot1	4096 MB (LPDDR3) Not Present	
		++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
Version 2.16.1	.242. Copyright (C) 201 <u>3 Americ</u>	an Megatrends, Inc.

#### **Memory Frequency Limiter**

Maximum memory frequency, set in MHz.

#### **Memory Remap**

Enables/Disables memory remap over 4G.

## **Onboard Device Configuration**

Aptio Setup Utility - Advanced	Copyright (C) 2013 Americ	an Megatrends, Inc.
Onboard devices configuration		Select COM1 mode. RS232, RS422
Serial Port Configuration		0 10 10 10
COM1 Device Settings	TO=3F8h; TRO=4;	
COM1 Control	[RS232]	
COM2 Device Settings	IO=2F8h; IRQ=3;	
COM2 Control	[RS232]	
COM3 Device Settings	IO=3E8h; IRQ=5;	
COM3 Control	[RS232]	
COM4 Device Settings	IO=2E8h; IRQ=7;	
COM4 Control	[RS232]	
Lan Port Configuration		
LAN #1 (Intel I211)	[Enabled]	
LAN #1(I211) Launch PXE OpROM	[Disabled]	++: Select Screen
LAN #2 (Intel I211)	[Enabled]	↑↓: Select Item
LAN #2(I211) Launch PXE OpROM	[Disabled]	Enter: Select
LAN #3 (Intel I211)	[Enabled]	+/-: Change Opt.
LAN #3(I211) Launch PXE OpROM	[Disabled]	F1: General Help
		F2: Previous Values
SATA Configuration	15 J. J. J.	F3: Uptimized Defaults
SATA Controller(S)	[Euapied]	F4: Save & Exit
UCB Configuration		ESU: EXIL
USB Devices:		
1 Drive 1 Keuboard 1 Mouse	4 Hubs	
2 Di 1VC, 1 Koybouru, 1 House,	1 11000	
Version 2.16.1242. C	opyright (C) 2013 American	Megatrends, Inc.

#### **COM1** Control

Select COM1 mode from among RS232, RS422 or RS485.

#### COM2 Control

Select COM2 mode from among RS232, RS422 or RS485.

#### LAN #1 (Intel I217)

Enables/disables onboard Intel I217 LAN controller.

#### Launch PXE OpROM

Enables/Disables execution of LAN boot-rom to add boot option for legacy network devices.

#### LAN #2 (Intel 210)

Enables/Disables onboard Intel 210 LAN controller.



#### Launch PXE OpROM

Enables/Disables execution of LAN boot-rom to add boot option for legacy network devices.

#### LAN #3 (Intel 210)

Enables/disables onboard Intel 210 LAN controller.

#### Launch PXE OpROM

Enables/disables execution of LAN boot-rom to add boot option for legacy network devices.

#### LAN #4 (Intel 210)

Enables/disables onboard Intel 210 LAN controller.

#### Launch PXE OpROM

Enables/disables execution of LAN boot-rom to add boot option for legacy network devices.

#### SATA Controller(s)

Enables/disables SATA Device.

#### Legacy USB Support

AUTO option disables legacy support if no USB devices are connected, DISABLE option keeps USB devices available only for EFI applications.

#### USB 3.0 Support

Enables/Disables USB 3.0 (XHCI) controller support, allowing

USB 3.0 devices to be used in DOS environment.

#### **XHCI Hand-Off**

Enables BIOS support of XHCI Hand-Off feature. Default option is Enabled.

#### **Advanced Power Management**

Aptio Setup Utility Advanced	– Copyright (C) 2013 America	an Megatrends, Inc.
Advanced Power Management Restore AC Power Loss Wake System With Fixed Time Wake On Ring BIOS POST Watchdog	[Power Off] [Disabled] [Disabled] [Disabled]	Select AC power state when power is re-applied after a power failure.
		<pre>++: Select Screen 11: Select Item Enter: Select +/-: Change Oot. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save &amp; Exit ESC: Exit</pre>
Version 2.16.1242.	Copyright (C) 2013 American	Megatrends, Inc.

#### **Restore On AC Power Loss**

Determines the state entered when power is restored after a power loss, from among Last State, Power On, and Power Off.

Power Off	When set, powers the system down when power is restored.	
Power On	When set, powers the system on when power is restored.	
Last State	When set, powers the system down or on depending on the last system power state when power is restored.	

#### Table B-1: Restore On Power Loss Settings



#### Wake System With Fixed Time

Enables/Disables System Wake on Alarm event. When enabled, system will wake at the hr::min::sec specified.

#### Wake On Ring

Disables/Enables RI ping for Wake On Ring function.

#### **OS Watchdog**

Enables/Disables OS Watchdog.

#### **BIOS POST Watchdog**

Sets watchdog timer for BIOS POST process.

## **SATA Configuration**

Aptio Setup Utility - Advanced	· Copyright (C) 2013 Americar	n Megatrends, Inc.
SATA Configuration		Select IDE / AHCI
SATA Mode SATA Port CF Card SATA Port CF Card	[AHOI] [Enabled] [Enabled] Not Present Not Present	
		++: Select Screen 11: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
Version 2.16.1242. C		

#### **SATA Mode Selection**

Allows selection of the SATA channel configuration from among (1) IDE Mode, (2) AHCI Mode, or (3) RAID Mode.

# SATA Port 0 / SATA Port 1 / CFAST Card / eSATA / mSATA / mSATA

Port X: Enables or disables SATA Port X

Hot Plug: Sets the port as hot pluggable.

#### **Serial Port Console Redirection**

	Aptio Setup Utility – Advanced	Copyright (C) 2013 American	Megatrends, Inc.
Seria	1 Port Console Redirection		Console Redirection Enable or Disable.
COM1 Conso ► Conso	le Redirection le Redirection Settings		
Seria Windo Conso Out-o Termi Bits Flow	I Port for Out-of-Band Management ws Emergency Management Service I Redirection if-Band Mgmt Port nal Type per second Control	nt/ s (EMS) [Disabled] [COM1] [VT-UTF8] [115200] [None]	
Data Parit Stop	Bits y Bits	8 None 1	<pre>++: Select Screen fl: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save &amp; Exit ESC: Exit</pre>
	Version 2.16.1242. Co	pyright (C) 2013 American M	egatrends, Inc.

#### **COM1 COM Console Redirection**

Enables/disables Console Redirection of COM1, from [Disabled (Default)/ Enabled]

Enabled:

Console Redirection Settings

Terminal Type

Selects transmission protocol for the remote terminal console from VT100/ VT100+/ VT-UTF8/ ANSI (Default)]

VT100



ASCII char set.

VT100+

Extends VT100 to support color, function keys, etc.

VT-UTF8

Uses UTF8 encoding to map unicode chars onto 1 or more bytes.

ANSI

Extended ASCII char set.

Bits per second

Selects serial port transmission speed, which must be matched on the other side, with long or noisy lines possibly requiring lower speeds, from [9600/ 19200/ 57600/ 115200 (Default)]

Data Bits, from [7/8 (Default)]

Parity

A parity bit can be sent with the data bits to detect some transmission errors, from [None (Default)/ Even/ Odd/ Mark/ Space]

Even

Parity bit is 0 if the num of 1's in the data bits is even.

Odd

Parity bit is 0 if num of 1's in the data bits is odd.

Mark

Parity bit is always 1.

Space

Parity bit is always 0.

Mark and Space Parity do not allow for error detection. They can be used as an additional data bit.

Stop Bits

Stop bits indicate the end of a serial data packet. (A start bit indicates the beginning). The standard setting is 1 stop bit. Communication with slow devices may require more than 1 stop bit, from [1 (Default)/ 2]

#### Flow Control

Flow control can prevent data loss from buffer overflow, from [None (Default)/ Hardware RTS/ CTS]

#### Recorder Mode

When enabled only text will be sent, to capture Terminal data, from [Disabled (Default)/ Enabled]

#### Resolution 100x31

Enables or disables extended terminal resolution, from [Disabled (Default)/ Enabled]

#### Legacy OS Redirection Resolution

On Legacy OS, the Number of Rows and Columns supported redirection, from [80x24 (Default)/ 80x25]

#### Putty KeyPad

Select Function Key and KeyPad on Putty, from [VT100 (Default)/ LINUX/XTERMR6/SCO/ESCN/VT400]

#### Redirection After BIOS POST

Specify if BootLoader is selected then Legacy console redirection is disabled before booting to Legacy OS. Default value is always Enable which means legacy console redirection is enabled for legacy OS, from [Always Enable (Default)/Boot-Loader]

#### (EMS) Console Redirection

Enables/disables Console Redirection of Out-of-Band Mgmt Port, from [Disabled (Default)/ Enabled]

#### Out-of-Band Mgmt Port

Microsoft Windows Emergency Management Services (EMS) allows remote management of a Windows Server OS through a serial port, from [COM1(Disable)(Default)/COM1(Pci Bus8,Dev0,Func0)]

#### Terminal Type

Selects transmission protocol for the remote terminal console from [VT100/ VT100+/ VT-UTF8 (Default)/ ANSI]



#### Bits per second

Selects serial port transmission speed, which must be matched on the other side, with long or noisy lines possibly requiring lower speeds, from [9600/ 19200/ 57600/ 115200 (Default)]

Flow Control

Can prevent data loss from buffer overflow, from [None (Default)/ Hardware RTS/ CTS]

#### **CSM** Configuration

Compatibility Support Module to support legacy Option ROM binaries and OS requiring a legacy BIOS runtime interface.



#### GateA20 Active

Switches memory access mode for compatible old and new OSs, from [Upon Request (Default)/ Always]

Upon Request

GateA20 can be disabled using BIOS services.

#### Always

Disallows disabling of GateA20, for when any RT code is executed above 1MB.

#### **Option ROM Messages**

Sets display mode for Option ROM, whether Option ROM information is displayed during boot, from [Force BIOS (Default)/ Keep Current]

#### **INT19 Trap Response**

BIOS reaction on INT19 trapping by Option ROM, from [Immediate (Default)/ Postponed]

Immediate

Executes trap immediately.

Postponed

Executes trap during legacy boot.

#### **Boot option filter**

Controls to what devices system can boot, from [UEFI and Legacy (Default)/ Legacy only/ UEFI only]

#### Launch Network OpROM policy

Controls the execution of UEFI and Legacy PXE OpROM, from [Do not launch(Default)/ UEFI only / Legacy only]

#### Launch Storage OpROM policy

Controls execution of UEFI and Legacy Storage OpROM, from [Do not launch(Default)/ UEFI only/ Legacy only]

#### Other PCI device ROM priority

For PCI devices other than Network, Mass storage, or Video, defines the OpROM to launch, from [UEFI only / Legacy only(Default)]



# System Management (SEMA)

HW monitoring information from ADLINK Smart Embedded Management Agent (SEMA)

Aptio Setup Utility Advanced	) – Copyright (C) 2013 Amer	rican Megatrends, Inc.
System Information SMC Bootloader SMC Firmware Temperatures Board Temperatures - Current - Startup - Min - Max Power Consumption CPU-Vcore GFX-Vcore OFX-Vcore V5.00 Vmem VBAT V3.30 V12.00 VIN Runtime Statistics Total Runtime Current Runtime Power Cycles	bl_ABX-1401 4v0 BMC ABX-1401 0v2 32 C 24 C 20 C 47 C 0.800 V 0.896 V 4.955 V 1.364 V 2.887 V 3.264 V 11.729 V 23.561 V 129 h 13 min 00 h 04 min 24 sec 758 cycles	++: Select Screen 1: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
Vanaion 3 16 1343	Community (c) costs among	an Madathanda Tha

# B.3 Chipset

<ul> <li>USB Configuration         High Precision Timer [Enabled]         Intel IGD Configuration         Integrated Graphics Device [Enabled]         IGD Turbo Enable [Enabled]         Primary Display [IGD]         GFX Boost [Disabled]         DVMT Pre-Allocated [64M]         DVMT Total Gfx Mem [256MB]         Aperture Size [256MB]         **: Select Screen 11: Select Item Entre: Select tem Entre: Select         +/-: Charge Duit,     </li> </ul>	
Integrated Graphics Device [Enabled] IGD Turbo Enable [Enabled] Primary Display [IGD] GFX Boost [Disabled] DVMT Pre-Allocated [G4M] DVMT Total Gfx Mem [256MB] Aperture Size [256MB] ++: Select Screen 11: Select Item Enter: Select tem Enter: Select +/-: Change Ont.	gs
F1: General Help F2: Previous Values F3: Optimized Defaults	_
F4: Save & Exit ESC: Exit	



# **USB** Configuration

Aptio Setup Utility – Main Advanced <mark>Chipset</mark> Security	Copyright (C) 2013 American Boot Save & Exit	Megatrends, Inc.
USB Configuration High Precision Timer Intel IGD Configuration	[Enabled]	USB Configuration Settings
Integrated Graphics Device IGO Turbo Enable Primary Display GFX Boost DVMT Total GfX Mem Aperture Size	[Enabled] [Enabled] [IGD] [Disabled] [64M] [256MB] [256MB]	
		++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
Version 2.16.1242. C	opyright (C) 2013 American M	egatrends, Inc.

#### **XHCI Mode**

xHCI controller mode of operation, from [Enabled/ Disabled/ Auto (Default)/ Smart Auto]

#### USB 2.0 (EHCI) Support

Controls the USB EHCI (USB 2.0) functions, wherein one EHCI controller must always be enabled, from [Disabled (Default)/ Enabled]

#### **High Precision Timer**

Enables/disables High Precision event timer, from [Enabled (Default)/Disabled]
### Intel IGD configuration

#### **Integrated Graphics Device**

[Enabled (Default)/Disabled]

When Enabled, Integrated Graphics Device (IGD) is the Primary Video Adaptor

#### **IGD Turbo Enable**

Enables/disables IGD Turbo, from [Enabled (Default)/Disabled]

#### Primary Display

Select the IGD/PCI Graphics device to be Primary Display, from [Auto/IGD (Default)/PCI]

#### **GFX Boost**

Enables/disables GFX Boots from [Enabled/Disabled (Default)]

#### **DVMT Pre-Allocated**

Selects DVMT 5.0 pre-allocated (fixed) graphics memory size used by the internal graphics device, from [64MB (Default)/ 96MB/ 128MB/ 160MB/ 192MB/ 224MB/ 256MB/ 288MB/ 320MB/ 352MB/ 384MB/ 416MB/ 448MB/ 480MB/ 512MB]

#### **DVMT Total Gfx Mem**

Sets DVMT5.0 total graphic memory size used by the Internal Graphics Device, from [128MB/ 256MB (Default)/ MAX]

### **Aperture Size**

Selects aperture size, from [128MB/ 256MB (Default)/ 512MB]



## **B.4 Security**

Aptio Setup Utili Main Advanced Chipset <mark>Secu</mark> r	ty – Copyright (C) 2013 Amer ity Boot Save & Exit	rican Megatrends, Inc.
Password Description		Set Administrator Password
If ONLY the Administrator's pas then this only limits access to only asked for when entering SE If ONLY the User's password is is a power on password and must boot or enter Setup. In Setup t have Administrator rights. The password length must be in the following range: Minimum length	sword is set, Setup and is tup. set, then this be entered to he User will 3	
Maximum length	20	++: Select Screen
Administrator Password User Password	<pre>fl: Select Item Enter: Select +/-: Change Opt. F1: General Help</pre>	
BIOS Read/Write Protection	[Disabled]	F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
Version 2.16.124	2. Copyright (C) 2013 Americ	an Megatrends, Inc.

If only the Administrator's password is set, only access to Setup is limited and the password only requested when entering Setup. If only the user's password is set, power on requires a password which must be accepted to boot or enter setup. In Setup the user has Administrator rights.

#### **Administrator Password**

Sets Administrator password

#### **User Password**

Sets boot/setup User password

#### **BIOS Read/Write Protection**

Allows only OEM proprietary utility update, from [Enabled/Disabled (Default)]

## B.5 Boot

Aptio Setup Utility – Main Advanced Chipset Security	Copyright (C) 2013 American Boot Save & Exit	Megatrends, Inc.
Boot Configuration Setup Prompt Timeout Bootup NumLock State	<b>1</b> [0n]	Number of seconds to wait for setup activation key. 65535(0xFFFF) means indefinite waiting.
Quiet Boot Fast Boot	[Enabled] [Disabled]	, in the second se
Boot Option Priorities Boot Option #1 Boot Option #2	[UEFI: Generic Flash] [Generic Flash Disk]	
Hard Drive BBS Priorities		
		↔: Select Scheen ↑↓: Select Item Enter: Select ↓ : Separa Opt
		F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit
		E30, EXIL
Version 2.16.1242. Co	pyright (C) 2013 American M	egatrends, Inc.

### **Boot Configuration**

#### **Setup Prompt Timeout**

Number of seconds to wait for setup activation key (DEL).

#### **Bootup Num-Lock State**

Allows Number Lock setting to be modified during boot.

#### Quiet Boot

When Disabled, directs BIOS to display POST messages. When Enabled, directs BIOS to display the OEM logo.

#### Fast Boot

Enables or disables boot with initialization of a minimal set of devices required to launch active boot option. Has no effect on BBS boot options.



### **Boot Option Priorities**

Specifies the priority of boot devices, with all installed boot devices detected during POST displayed. Boot Option # specifies the desired boot device.

## B.6 Exit

Aptio Setup Utility – Copyright (C) 2013 American Main Advanced Chipset Security Boot <mark>Save &amp; Exit</mark>	Megatrends, Inc.
Discand Changes and Exit Save Changes and Reset Discard Changes	
Restore Defaults Save as User Defaults Restore User Defaults	
Boot Override UEFI: Generic Flash Disk 8.07 Generic Flash Disk 8.07	
Launch EFI Shell from filesystem device	++: Select Screen f1: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
Version 2.16.1242. Copyright (C) 2013 American Ma	egatrends, Inc.

#### **Discard Changes and Exit**

Exits system setup without saving any changes

#### Save Changes and Reset

Resets the system after saving the changes

#### **Discard Changes**

Discards changes made to any of the Setup options.

#### **Restore Defaults**

Returns all BIOS options to default settings, designed for maximum system stability, but not performance. Applicable in the event of system configuration problems.

#### Save as User Defaults

Saves changes made as User Defaults

#### **Restore User Defaults**

Restores User Default settings to all Setup options

#### Launch EFI Shell from filesystem device



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# Appendix C SEMA Utility GUI

The SEMA graphical interface is available for Windows operating systems. To get started in Windows, simply run SEMAGui.exe

## C.1 System Overview Tab



### C.1.1 Board Information

This section shows general information about the board, BIOS Version and SEMA features supported. The first three lines show the firmware versions of the board. The BMC versions each contain an ID-string and the respective build date. The next lines show



the board ID, hardware revision, serial number, manufacturing/ repair date and MAC ID of the board. The last line shows all supported SEMA features for this board.

## C.2 System Health Tab

SEMA - Sm	art Embedded	Management A	Agent - 2.5 R4		
bout					
Overview	System Health	User Flash	Hardware Cor	trols Hardware Mo	onitor I2C Bus GPIO
Temperatur	es & Fan Speed				
	Curi	ent	Startup	Min	Max
CPU	42.0	)°C	N/A	N/A	N/A
Board	429	C	25°C	0°C	0°C
CPU Fan Sp	eed N/A				
System Far	ns Speed ORP	м	ORPM	ORPM	
Power Con	sumption				
	Curre	ent Average ove	er 255s 🌲		
Input curre	nt OmA	10mA			
Input Powe	er OW	0.12W			
-Runtime St	atistics				
Total Runti	me 5171	5733h 20m			
Current Ru	ntime 9081	27h 40m 35s			
Power Cyc	es 3084				
Boot Cycle	s 0				
Boot Reaso	on Softv	/are Reset			
Flags & Sta	tus				
BMC Flags	0x40				Clear
- BIOS Sele	ect N/A				
- ATX/AT M	ode N/A				
- Exception	Code 0x00				
BMC Status	s 0x00				
- Exception BMC Status			ЕМА		Exit
					BIOS Ok!

### C.2.1 Temperatures & Fan Speed

This section displays the current, start-up, minimum and maximum temperatures of the CPU and environment (board). The data is displayed in °C and updated every second. Current CPU and speeds of system fans 1 to 3 are also displayed in RPM. Note that

not all platforms supply all information as shown, in which case  $\,$  N/ A is displayed in lieu of the value.

### C.2.2 Power Consumption

Displays information for the main power supply in mA for current and W for power consumption, and are updated every second. To filter out spikes and acquire average values over a certain interval of time, a second/sample value can be selected from 0 to 255. Averaging takes place within the graphical user interface rather than within the BMC itself. The application collects the selected number of samples and present an average thereof. Note that not all platforms may provide the necessary current sensors, in which case N/A is displayed and the section will grayed out.

### C.2.3 Runtime Statistics

Total	Total system uptime in hours and minutes
Current	Uptime since last boot in hours, minutes and seconds
Power cycles	Number of power cycles
Boot cycles	Number of HW/SW-Resets and successful power-ups
Boot reason	Reason for the last reboots (e.g. power loss, power down, HW reset, etc.)

### C.2.4 Flags

Display board-specific information about the internal status of the BMC. Please refer to the technical manual for the board, the chapter entitled "SEMA functions". Also provided is information regarding the currently active BIOS, whether the system is in AT or ATX mode, and Exception Codes, if applicable.



## C.3 User Flash Memory Tab



The Read/Write Raw Data Storage in SEMA Flash Memory function is reserved for advanced SEMA applications.

The Read/Write Raw Data Storage in SEMA Flash Memory function is reserved for advanced SEMA applications.

Overview	S	ysten	Heal	th	User	Flash		Hardv	vare C	Contro	ls	Hard	ware	Monit	or	I2C	Bus GPIO
Read			Addre	ss 0x	0			Si	ize 0x	010	0						
141-24-2	_																
write																	
Clear																	
Address	00	01	02	03	04	05	06	07	08	09	0A	0B	0C	0D	0E	0F	ASCII
0x0000	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	•••••
0x0010	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	
0x0020	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	
0x0030	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	
0x0040	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	•••••
0x0050	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	•••••
0x0060	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	•••••
0x0070	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	•••••
0x0080	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	
0x0090	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	•••••
0x00A0	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	
0x00B0	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	•••••
0x00C0	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	•••••
0x00D0	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	•••••
0X00E0	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	•••••
0x00F0	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	•••••

### C.3.1 Memory

For Flash Memory organization, with address and size entered as hexadecimal values. Valid start addresses are multiples of 16 within the range: 0x0000-0x01F0 for 512 byte variants and 0x0000-0x03F0 for 1024 byte variants. Size can be any multiple of 16 bytes (up to the total memory size).

For example, to display the first 32 bytes from user flash memory, enter "0000" as address, "20" (hex 20 equals decimal 32) as size and press the [Read] button. Content is acquired from the BMC and displayed in the output window.

Each line contains the address of the first byte of this line, followed by 16 data bytes (displayed as hexadecimal values, with prefixes "0x" omitted for clarity) followed by the corresponding printable ASCII characters. The [Write] button opens the Write memory dialog box.

## C.3.2 Write Memory

🏧 Write Fla	sh Memory	? ×
Address 0x	0000	Data mode
Size Padding	Auto	<ul> <li>String</li> <li>Hex</li> </ul>
Data		
	ОК	Cancel

### Address

The address is entered as hexadecimal value within the range of 0x0000..0x01FC (respectively 0x03FC for 1k board variants)

### Size

Can be any multiple of four, up to 32 bytes or "Auto". In Auto mode, the number of bytes to write is determined by the amount of data entered in the subsequent data field (max 32 bytes).



### Padding

If the data length is not a multiple of four, the required number of pad (or fill) characters is appended. In String mode, a valid pad character can be any printable ASCII character or a predefined pad characters: - SPACE character (ASCII character 0x20) - NULL character (ASCII character 0x00) In Hex mode, any eight bit hex value (00..FF) is valid.

#### Data Mode

Currently supported modes are String mode, in which entered text (up to 32 characters) is converted to a byte stream, with no termination character appended, and Hex mode, in which up to 64 hex digits (0 to 9, A to F) can be entered, resulting in up to 32 bytes of data.

## C.4 Hardware Control Tab

SEMA - Smart Embedde	d Management Agent - 2.5 R4		
About			
Overview System Heal	th User Flash Hardware Controls	Hardware Monitor	I2C Bus GPIO
Enable Watchdog			
Watchdog			
Timeout 0s	Set Set		
Power Up Watchdog			
Timeout 10	s 🚔 Set		
LVDS Backlight Control			
255 🚔			
Dark			Bright
EMC			
I2C			
Smart Fan			
CPU Fan	Trigger Settings		
System Fan #1	Auto	Trigger Temp.	PWM Level
System Fan #2	Fan On     Trigger Point 1	0℃	0%
System Fan #3	Fan Off Trigger Point 2	0℃	0%
Temperature Source	Trigger Point 3	0°C	0%
Board Temp.	Trigger Point 4	0°C	0%
OPU Temp.			
		Read	Values Store Values
TECHNOLO			Exit
			O BIOS Ok!

### C.4.1 Watchdog

The watchdog timeout value is given in seconds and can be set at 1 to 65535 seconds, with a value of 0 seconds disabling the watchdog. When enabled, the watchdog should be triggered repeatedly within the timeout period to avoid system reset. Note that not all platforms support watchdog functionality, in which case the section is grayed out.



NOTE

►	When using the watchdog feature, all partitions
	must be mounted as read-only, to avoid file sys-
	tem corruption and data loss.

It is not advisable to use the watchdog feature in a Windows environment, since Windows restart uses a safe shutdown procedure.

### C.4.2 Backlight Control

Enables or disables the backlight of display connected via LVDS, with value adjusted by a slider or entered as a numerical, from 0 to 255. Note that not all platforms support the necessary circuitry, in which case the section is grayed out. Radio boxes allow selection of control by BMC or LVDS I2C interface.

### C.4.3 Smart Fan

Provides control of either CPU or system fans (if applicable). The accessible system is also listed in SEMA Features in the System Overview Tab or via the "sema cap" command line

All fans can have a discrete independent temperature source defining the temperature sensor used for calculating speed.

Settings are as shown

Setting	Function
Auto	No other settings required
Off	Fan is disabled
On	Fan runs at maximum speed

Trigger Temp and PWM Level determine the characteristic curve of the fan control. Sample settings as shown result in fan function as follows.

Temperature	Fan PWM level
Falls below 15°C	Turns off
Between 15°C and 60°C	30%
Between 40°C and 70°C	40%
Between 48°C and 80°C	63%
Exceeds 55°C	100%



If the temperature drops below one of the trigger points, the lower PWM value is taken.



## C.5 Hardware Monitor Tab

Shows Power Consumption and Temperatures information from the System Health Tab in a graphical format.



When logging is enabled, all values queried every second, such as temperatures and power consumption, are recorded to a log file.

At startup, logging is disabled by default.

To enable, check Enable Logging and select a file. New data is appended to the selected file, with existing data not overwritten. Logging ceases when Enable Logging is unchecked or the program closed, and suspended during a File Open dialog. Data is Recorded as simple ASCII text in tab-separated columns for easy import/export into any spreadsheet calculation program or other data processing tool. The first line written to the log file contains captions indicating the content and the respective unit of the data to be recorded. When any data is unavailable, such as when, for example, no sensor is detected, the corresponding column is skipped.

Currently available items are as follows

Caption	Content/Unit
Total[m]	Total uptime in minutes
PwrUp[s]	Uptime since last power up in seconds
Tcpu[°C]	CPU temperature in °C
Tbrd[°C]	Board temperature in °C
Tcpumin[°C]	Minimal CPU temperature in °C
Tcpumax[°C]	Maximal CPU temperature in °C
Tbrdmin[°C]	Minimal board temperature in °C
Tbrdmax[°C]	Maximal board temperature in °C
Imp[mA]	Main power current in mA
Pmp[W]	Power on main power current rail in W
n[1]	Number of samples used for filtering
Impf[mA]	Filtered main power current in mA
Pmpf[W]	Filtered power on main power current rail in W
FanSpdSys1-3[RPM]	CPU-Fan speed in RPM
FanSpdCPU[RPM]	System-Fan speed in RPM
'VoltageName'[V]	For each monitored voltage in V

## C.6 I<sub>2</sub>C Bus Tab

The board management controller (BMC) can access up to four external  $I_2C$  busses, with byte and block access for read and write implemented. Both common bus speeds of 100 and 400kHz are supported.



To read data from or write data to the BMC an address (8-bit) must be given as well as the amount of data to be transferred (length) plus the register offset.

Data should then be handed over in hex values forming a hex string, as shown.

🚟 SEMA - Smart Embedded Manager	nent Agent - 2.5 R4	
About		
Overview System Health User	Flash Hardware Controls	Hardware Monitor I2C Bus GPIO
Bus Selection	Transfer Type	I2C Bus Speed
I2C Bus 1	Read/Write Block	I00KHz
I2C Bus 2	Read/Write Byte	400KHz
I2C Bus 3	Read/Write Command - I	Byte
I2C Bus 4		
I2C Data		
Address (8Bit) Length	Register / Command	
0x 00 0x 00 0x	c 00	
Data		
0x  ##-##-##-##-##-##-##	-##-##-##-##-##-##-##-#	#
Read		
Write		Transmit
	Sema	
		Exit
		SIOS Ok!

Transfer Types

Different transfer types will result in  $I_2C$  bus activity as follows.

Read/Write Block	Write	Start + Address/Write + Register /Command + Length + Data[1] + Data[2] + +Data[Length] + Stop
	Read	Start + Address/Write + Register /Command + Start + Address/Read + Length + Data[1] +Data[2] + + Data[Length] + Stop
Read/Write Byte	Write	Start + Address/Write + Data[1] + Stop
	Read	Start + Address/Read + Data[1] + Stop
Read/Write	Write	Start + Address/Write + Register /Command + Data[1] + Stop
Command Byte	Read	Start + Address/Read + Register /Command + Data[1] + Stop



 $\rm I_2C$  Bus 3 does not support Read/Write Command Byte transfer.

## C.7 GPIO Tab

Allows control of up to 16 GPIOs provided by the ADLINK board, with the actual number of usable GPIOs board-dependent.



verview	System H	lealth	User Flash	Hardware	Controls	Hardware Monitor	I2C Bus	GPIO
GPIO infor	mation							
Capabilitie	s (Hex)	Di	ection for now	<i>r</i> : 0	0000 x		Get	Direction
in :	0x	FFFF In	put New Direct	tion :	0000 x		Set	Direction
out :	0x	0000						
IO Read								
Value (Hex	):	0x 00	00				Ge	et Values
O Write								
New Value	(Hex) :	0x 000	00				Wr	ite Values
IO XorAnd	Xor							
Xor1:		0x 000	00					
And2 :		0x 000	00					
Xor3:		0x 000	00				Wr	ite Values

### C.7.1 GPIO information

Controls GPIO direction, with each bit in the shown byte representing a GPIO.

To set a GPIO for output the bit must set to 0, and for input 1. Get Direction reads the current configuration and Set Direction sets the configuration to the value entered in the Input New Direction field.

## C.7.2 IO Read

Shows the current input values for all GPIOs, with those configured as output showing current output value.

## C.7.3 IO Write

Sets outputs, with GPIOs configured as input not affected.

## C.7.4 IO XorAndXor

Reads the GPIO input and applies the logic operation in order of the input fields, with the result set as output value.

## C.8 Status Bar

Displays a flashing red LED if the board has booted using the Failsafe BIOS, as may occur if the main BIOS has been corrupted.





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# **Important Safety Instructions**

For user safety, please read and follow all instructions, Warnings, Cautions, and Notes marked in this manual and on the associated device before handling/operating the device, to avoid injury or damage.

S'il vous plaît prêter attention stricte à tous les avertissements et mises en garde figurant sur l'appareil, pour éviter des blessures ou des dommages.

- Read these safety instructions carefully
- ► Keep the User's Manual for future reference
- Read the Specifications section of this manual for detailed information on the recommended operating environment
- The device can be operated at an ambient temperature of 50°C
- When installing/mounting or uninstalling/removing device; or when removal of a chassis cover is required for user servicing (See "Getting Started" on page 25.):
  - ▷ Turn off power and unplug any power cords/cables
  - > Reinstall all chassis covers before restoring power
- ► To avoid electrical shock and/or damage to device:
  - ▷ Keep device away from water or liquid sources
  - ▷ Keep device away from high heat or humidity
  - Keep device properly ventilated (do not block or cover ventilation openings)
  - Always use recommended voltage and power source settings
  - Always install and operate device near an easily accessible electrical outlet
  - Secure the power cord (do not place any object on/over the power cord)
  - Only install/attach and operate device on stable surfaces and/or recommended mountings
- If the device will not be used for long periods of time, turn off and unplug from its power source



- Never attempt to repair the device, which should only be serviced by qualified technical personnel using suitable tools
- A Lithium-type battery may be provided for uninterrupted backup or emergency power.



Risk of explosion if battery is replaced with one of an incorrect type; please dispose of used batteries appropriately. *Risque d'explosion si la pile est remplacée par une autre de type incorrect. Veuillez jeter les piles usagées de façon appropriée.* 

- The device must be serviced by authorized technicians when:
  - ▷ The power cord or plug is damaged
  - Liquid has entered the device interior
  - The device has been exposed to high humidity and/or moisture
  - The device is not functioning or does not function according to the User's Manual
  - The device has been dropped and/or damaged and/or shows obvious signs of breakage
- Disconnect the power supply cord before loosening the thumbscrews and always fasten the thumbscrews with a screwdriver before starting the system up
- It is recommended that the device be installed only in a server room or computer room where access is:
  - Restricted to qualified service personnel or users familiar with restrictions applied to the location, reasons therefor, and any precautions required
  - Only afforded by the use of a tool or lock and key, or other means of security, and controlled by the authority responsible for the location



#### **BURN HAZARD**

Touching this surface could result in bodily injury. To reduce risk, allow the surface to cool before touching.

### **RISQUE DE BRÛLURES**

Ne touchez pas cette surface, cela pourrait entraîner des blessures. Pour éviter tout danger, laissez la surface refroidir

avant de la toucher.



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# **Getting Service**

Ask an Expert: http://askanexpert.adlinktech.com

#### ADLINK Technology, Inc.

9F, No.166 Jian Yi Road, Zhonghe District
New Taipei City 235, Taiwan
新北市中和區建一路 166 號 9 樓
+886-2-8226-5877
+886-2-8226-5717
service@adlinktech.com

#### Ampro ADLINK Technology, Inc.

Address:	5215 Hellyer Avenue, #110
	San Jose, CA 95138, USA
Tel:	+1-408-360-0200
Toll Free:	+1-800-966-5200 (USA only)
Fax:	+1-408-360-0222
Email:	info@adlinktech.com

#### ADLINK Technology (China) Co., Ltd.

Address:	上海市浦东新区张江高科技园区芳春路 300 号 (201203)
	300 Fang Chun Rd., Zhangjiang Hi-Tech Park
	Pudong New Area, Shanghai, 201203 China
Tel:	+86-21-5132-8988
Fax:	+86-21-5132-3588
Email:	market@adlinktech.com

#### ADLINK Technology Beijing

Address:	北京市海淀区上地东路 1 号盈创动力大厦 E 座 801 室(100085)
	Rm. 801, Power Creative E, No. 1 Shang Di East Rd.
	Beijing, 100085 China
Tel:	+86-10-5885-8666
Fax:	+86-10-5885-8626
Email:	market@adlinktech.com

#### ADLINK Technology Shenzhen

Address:	深圳市南山区科技园南区高新南七道 数字技术园
	A1栋2楼C区 (518057)
	2F, C Block, Bldg. A1, Cyber-Tech Zone, Gao Xin Ave. Sec. 7
	High-Tech Industrial Park S., Shenzhen, 518054 China
Tel:	+86-755-2643-4858
Fax:	+86-755-2664-6353
Email:	market@adlinktech.com

#### LiPPERT ADLINK Technology GmbH

Address:	Hans-Thoma-Strasse 11
	D-68163 Mannheim, Germany
Tel:	+49-621-43214-0
Fax:	+49-621 43214-30
Email:	emea@adlinktech.com

#### PENTA ADLINK Technology GmbH

	Ulrichsbergerstrasse 17
	94469 Deggendorf, Germany
Tel:	+49 (0) 991 290 94 - 10
Fax:	+49 (0) 991 290 94 - 29
Email:	emea@adlinktech.com



#### ADLINK Technology, Inc. (French Liaison Office)

Address:	6 allée de Londres, Immeuble Ceylan
	91940 Les Ulis, France
Tel:	+33 (0) 1 60 12 35 66
Fax:	+33 (0) 1 60 12 35 66
Email:	france@adlinktech.com

#### **ADLINK Technology Japan Corporation**

Address:	〒101-0045 東京都千代田区神田鍛冶町 3-7-4
	神田 374 ビル 4F
	KANDA374 Bldg. 4F, 3-7-4 Kanda Kajicho,
	Chiyoda-ku, Tokyo 101-0045, Japan
Tel:	+81-3-4455-3722
Fax:	+81-3-5209-6013
Email:	japan@adlinktech.com

#### ADLINK Technology, Inc. (Korean Liaison Office)

Address:	경기도 성남시 분당구 수내로 46 번길 4 경동빌딩 2 층
	(수내동 4-4 번지) (우) 463-825
	2F, Kyungdong B/D, 4 Sunae-ro 46 beon-gil
	Bundang-gu, Seongnam-si, Gyeonggi-do, Korea, 463-825
Toll Free	+82-80-800-0585
Tel	+82-31-786-0585
Fax	+82-31-786-0583
Email:	korea@adlinktech.com

#### ADLINK Technology Singapore Pte. Ltd.

Address:	84 Genting Lane #07-02A, Cityneon Design Centre
	Singapore 349584
Tel:	+65-6844-2261
Fax:	+65-6844-2263
Email:	singapore@adlinktech.com

#### ADLINK Technology Singapore Pte. Ltd. (Indian Liaison Office)

Address:	#50-56, First Floor, Spearhead Towers
	Margosa Main Road (between 16th/17th Cross)
	Malleswaram, Bangalore - 560 055, India
Tel:	+91-80-65605817, +91-80-42246107
Fax:	+91-80-23464606
Email:	india@adlinktech.com

#### ADLINK Technology, Inc. (Israeli Liaison Office)

Address:	27 Maskit St., Corex Building
	PO Box 12777
	Herzliya 4673300, Israel
Tel:	+972-54-632-5251
Fax:	+972-77-208-0230
Email:	israel@adlinktech.com

#### ADLINK Technology, Inc. (UK Liaison Office)

Tel:	+44 774 010 59 65
Email:	UK@adlinktech.com