

Specifications

Model Number	PXI-2208
Analog Input	
Resolution	12 bits, no missing codes
Number of channels	96 single-ended or 48 differential (software selectable per channel)
Channel gain queue size	1024
Maximum sampling rate	3 MS/s
Programmable gain	1, 2, 4, 5, 8, 10, 20, 40, 50, 200
Bipolar input ranges	± 10 V, ± 5 V, ± 2.5 V, ± 1.25 V, ± 1 V, ± 0.5 V, ± 0.25 V, ± 0.2 V, ± 0.05 V
Unipolar input ranges	0-10 V, 0-5 V, 0-4 V, 0-2.5 V, 0-2 V, 0-1 V, 0-0.5 V, 0-0.4 V, 0-0.1 V
Offset error	± 1 mV
Gain error	± 0.03 % of FSR
Input Coupling	DC
Overvoltage protection	Power on: continuous ± 30 V, Power off: continuous ± 15 V
Input Impedance	1 G Ω /100 pF
CMRR (gain = 1)	90 dB
Settling time	1 μ s to 0.1 % error *
-3dB small signal bandwidth (gain = 1)	2 MHz
Trigger sources	Software, external digital/analog trigger, PXI trigger bus
Trigger modes	Pre-trigger, post-trigger, middle-trigger, delay-trigger, and repeated trigger
FIFO buffer size	1 k samples
Data Transfers	Polling, scatter-gather DMA
Digital I/O	
Number of channels	24-CH 8255 programmable input/output
Compatibility	5 V/TTL
Data transfers	Programmed I/O
Auto Calibration	
On-board reference	+5 V
Temperature drift	± 2 ppm/ $^{\circ}$ C
Stability	± 6 ppm/1000 Hrs
General Specifications	
Dimensions	160 mm x 100 mm (not including connectors)
Connector	68-pin VHDCI female x 2
Operating temperature	0 to 55 $^{\circ}$ C
Storage temperature	-20 to 80 $^{\circ}$ C
Humidity	5 to 95 %, noncondensing
Power requirements	+5 V 1.3 A typical

*Gain = 1, 2, 4, 8

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