

# PXI-2200 Series

## 64-CH, 12/16-Bit, Up to 3 MS/s Multi-Function PXI Modules

### Features

- PXI specifications Rev. 2.0 compliant
- 3U Eurocard form factor, CompactPCI compliant (PICMG 2.0 R3.0)
- 64-CH single-ended or 32-CH differential analog inputs
- Up to 3 MS/s sampling rate (PXI-2204)
- 12-bit A/D resolution (PXI-2204)
- 16-bit A/D resolution (PXI-2205 and PXI-2206)
- On-board 1k-sample A/D FIFO
- Bipolar or unipolar analog input ranges
- Programmable gains:
  - •x1, x2, x4, x5, x8, x10, x20, x40, x50, x200 (PXI-2204)
  - •x1, x2, x4, x8 (PXI-2205 and PXI-2206)
- 512-configuration channel-gain queue
- Scatter-gather DMA for both analog inputs and outputs
- 2-CH 12-bit multiplying analog outputs with waveform generation
- On-board 1k-sample D/A FIFO
- 24-CH TTL digital input/output
- 2-CH 16-bit general purpose timer/counter
- Analog and digital triggering
- Fully auto calibration
- Multiple modules synchronization through PXI trigger bus

### Operating Systems

- •Windows 2000/NT/XP/98
- •Red Hat Linux
- •Windows CE (call for availability)

### Recommended Software

- •d/B/C++/BCB/Delphi
- •DAQBench

### Driver Support

- •D2K-DASK:
  - Windows 2000/NT/XP/98 driver
- •D2K-DASK/X: Red Hat Linux driver
- •D2K-LVIEW: LabVIEW driver
- •D2K-MTLB: MATLAB driver
- •D2K-OCX: 32-bit ActiveX controls



### Introduction

ADLINK PXI-2204, PXI-2205, and PXI-2206 are high-density, high-performance multifunction PXI modules. These devices can sample up to 64 AI channels with various gain settings and scan sequences---making them ideal for high-density analog signals with various input ranges and sampling speeds. These devices also offer a differential mode for 32 AI channels to achieve maximum noise elimination.

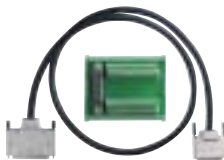
The PXI-2200 series also features analog and digital triggering, 2-CH 12-bit analog outputs with waveform generation capabilities, 24-CH programmable digital I/O lines, and 2-CH 16-bit general-purpose timers/counters.

Like all the other members in the PXI-2000 family, the PXI-2200 series are able to perform analog input and output functions at full speed simultaneously. Multiple modules can also be synchronized through the PXI trigger bus. The auto-calibration feature adjusts the gain and offset to a specified accuracy, eliminating the need to calibrate the modules by adjusting trimpots.

### Termination Boards

#### DIN-68S/1M

Termination Board with a 68-pin SCSI-II Connector and DIN-Rail Mounting (Including One 1-meter ACL-10568 Cable)



Termination board DIN-68S/1M

### Ordering Information

- **PXI-2204**  
64-CH 12-Bit 3 MS/s Multi-Function PXI Module
- **PXI-2205**  
64-CH 16-Bit 500 kS/s Multi-Function PXI Module
- **PXI-2206**  
64-CH 16-Bit 250 kS/s Multi-Function PXI Module

### Pin Assignment

#### Connector CN1 Pin Assignment

AI0 (AIH0)	1	35	(AIL0)	AI32
AI1 (AIH1)	2	36	(AIL1)	AI33
AI2 (AIH2)	3	37	(AIL2)	AI34
AI3 (AIH3)	4	38	(AIL3)	AI35
AI4 (AIH4)	5	39	(AIL4)	AI36
AI5 (AIH5)	6	40	(AIL5)	AI37
AI6 (AIH6)	7	41	(AIL6)	AI38
AI7 (AIH7)	8	42	(AIL7)	AI39
AI8 (AIH8)	9	43	(AIL8)	AI40
AI9 (AIH9)	10	44	(AIL9)	AI41
AI10 (AIH10)	11	45	(AIL10)	AI42
AI11 (AIH11)	12	46	(AIL11)	AI43
AI12 (AIH12)	13	47	(AIL12)	AI44
AI13 (AIH13)	14	48	(AIL13)	AI45
AI14 (AIH14)	15	49	(AIL14)	AI46
AI15 (AIH15)	16	50	(AIL15)	AI47
AISENSE				17 51 AIGND
AI16 (AIH16)	18	52	(AIL16)	AI48
AI17 (AIH17)	19	53	(AIL17)	AI49
AI18 (AIH18)	20	54	(AIL18)	AI50
AI19 (AIH19)	21	55	(AIL19)	AI51
AI20 (AIH20)	22	56	(AIL20)	AI52
AI21 (AIH21)	23	57	(AIL21)	AI53
AI22 (AIH22)	24	58	(AIL22)	AI54
AI23 (AIH23)	25	59	(AIL23)	AI55
AI24 (AIH24)	26	60	(AIL24)	AI56
AI25 (AIH25)	27	61	(AIL25)	AI57
AI26 (AIH26)	28	62	(AIL26)	AI58
AI27 (AIH27)	29	63	(AIL27)	AI59
AI28 (AIH28)	30	64	(AIL28)	AI60
AI29 (AIH29)	31	65	(AIL29)	AI61
AI30 (AIH30)	32	66	(AIL30)	AI62
AI31 (AIH31)	33	67	(AIL31)	AI63
EXTATRIG	34	68	AIGND	

### Pin Assignment

#### Connector CN2 Pin Assignment

DA0OUT	1	35	AOGND
DA1OUT	2	36	AOGND
AOEXTREF	3	37	AOGND
N/C	4	38	N/C
DGND	5	39	DGND
EXTWFTTRIG	6	40	DGND
EXTDTRIG	7	41	DGND
SSHOUT	8	42	SDI0 / DGND*
RESERVED	9	43	SDI1 / DGND*
RESERVED	10	44	SDI2 / DGND*
AFI1	11	45	SDI3 / DGND*
AFI0	12	46	DGND
GPTC0_SRC	13	47	DGND
GPTC0_GATE	14	48	DGND
GPTC0_UPDOWN	15	49	DGND
GPTC0_OUT	16	50	DGND
GPTC1_SRC	17	51	DGND
GPTC1_GATE	18	52	DGND
GPTC1_UPDOWN	19	53	DGND
GPTC1_OUT	20	54	DGND
EXTTIMEBASE	21	55	DGND
PB7	22	56	PB6
PB5	23	57	PB4
PB3	24	58	PB2
PB1	25	59	PB0
PC7	26	60	PC6
PC5	27	61	PC4
DGND	28	62	DGND
PC3	29	63	PC2
PC1	30	64	PC0
PA7	31	65	PA6
PA5	32	66	PA4
PA3	33	67	PA2
PA1	34	68	PA0

\*Pin 42-45 are SDI<0..3> for PXI-2204 ;  
DGND for PXI-2205 and PXI-2206