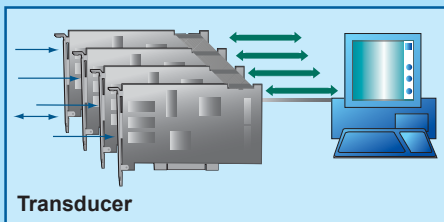




PXI-2000 Series

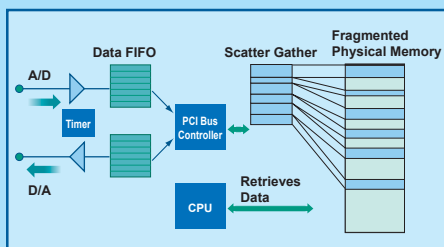
Multiple card synchronization

The DAQ-2000 series provide high-speed signal bus for trigger and clock transmission. This interface allows you to integrate multiple cards into a system with card-to-card synchronization.



Bus-mastering DMA with scatter-gather

The scatter-gather bus mastering DMA can transfer data to and from fragmented physical memory intelligently without CPU intervention. The DMA controller has a built-in linking table that tells DMA controller the memory address and the number of data to transfer, and it can automatically load the next linking address to transfer data to or from the next memory segment. With this mechanism, scatter-gather bus mastering allows the DMA controller to transfer data to and from the memory up to the size limited by the system memory. Therefore it allows user to acquire high speed, huge amount of data into the cost effective system memory.



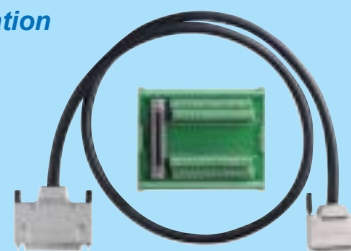
Small footprint

The piggy-back configuration of the DAQ-2000 not only minimizes the noise induced from digital circuitry but also reduces the size of the board. This allows you to integrate the DAQ-2000 into half size computer chassis to meet the applications when the space is a concern.

Easy upgrade to PXI/CompactPCI form factor

By taking the advantage of carrier and daughter card design, the DAQ-2000 can be upgraded to PXI/CompactPCI form factor easily. (PXI-2000 series) The software is fully compatible such that the modification of program is not needed at all.

Wiring termination DIN-68S/1M



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