[ANALOG I/O] ISA Bus

For the optional items, please refer to Page J-08 (accessories and cables) and Page G-01 (support software).

ISA

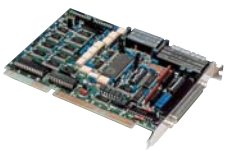
Model	8 Single-Ended Analog to Digital Input Board AD12-8LT(PC)	Analog to Digital Input Board AD12-16(PC)	Low Cost 16ch Single-ended Analog to Digital Input Board AD12-16LG(PC)
			

Specification

Input channels		8 single-ended input	16 single-ended or 8 differential input	16 single-ended input
Resolution		12bit		
Input specification	**Input range**	±5V	±5V, 0~5V, 0~10V	±5V
	Input gain	-		x1, x2, x4, x8, x16 (specifiable bye software for each channel)
	Conversion speed	25μsec/ch	20μsec/ch	15μsec/ch
	Non-Learity error *1	±3LSB	±2LSB	±3LSB
	Input Impedance	100MΩ or more	1MΩ or more	100MΩ or more
Trigger		1 TTL-level		
Isolation		-		
Timer		16-bit down counter x3 (82C54 or equivalent)	2~7x10^{13}μsec	
Digital I/O		3 TTL-level input, 4 COMS-level output (Negative logic)	1 TTL-level input/output (Negative logic)	8 TTL-level input/output (Positive logic)
Interrupt	**Interrupt Request Causes**	External trigger	External trigger / Timer / Transformation end	Transformation end
	Interrupt Request Level	One of IRQ 3~7, 9		
I/O address		Any 8-byte boundary	Any 16-byte boundary	Any 8-byte boundary
Power consumption (Max.)		+5VDC 400mA, +12VDC 15mA	+5VDC 700mA	+5VDC 400mA, +12VDC 25mA
Bus / Dimension (mm)		XT Bus / 130.0(L) x 107.0(H)	XT Bus / 143.0(L) x 107.0(H)	
Connector		37-pin male D-type	37-pin female D-type	37-pin male D-type
Option	**Software**	ACX-PAC(W32)BP, ACX-PAC(W32)AP, API-PAC(W32), SUPPORT-PAC(PC)305, DDE SERVER(W32)	ACX-PAC(W32)BP, ACX-PAC(W32)AP, API-PAC(W32), SUPPORT-PAC(PC)301, DDE SERVER(W32)	ACX-PAC(W32)BP, ACX-PAC(W32)AP, API-PAC(W32), SUPPORT-PAC(PC)304, DDE SERVER(W32)
	Accessories	DTP-3(PC) *2, DTP-4(PC) *2, EPD-37 *2	DTP-3(PC), DTP-4(PC), EPD-37 *3	DTP-3(PC) *2, DTP-4(PC) *2, EPD-37 *2
	Cables / Connector	DT-6, DT-11	PCA37P, PCB37P, PCA37PS, PCB37PS	DT-6, DT-11
CE marking		-	○	○

*1: Conversion Accuracy: A value in the table is linearity error at 25 . *2: Option cable of DT-6 is required.
*3: Option cable of PCB37P or PCB37PS is required.

ISA

Model	Isolated Analog to Digital Input Board ADI12-8CL(PC)H	Opto-Isolated Analog to Digital Input Board ADI12-16(PC)
		

Specification

Input channels		8 single-ended input	16 single-ended or 8 differential input
Resolution		12bit	
Input specification	**Input range**	0~5V, 1~5V, 0~20mA, 4~20mA	±5V, ±10V, 0~10V, 4~20mA
	Input gain	-	
	Conversion speed	1200μsec/ch	25μsec/ch
	Non-Learity error *1	±3LSB	
	Input Impedance	1MΩ or more	
Trigger		1 opto-isolated input (share 1 of digitalinput)	1 opto-isolated input (shared signal of Rising-edge or digital input)
Isolation		Individual isolation	PCI Bus signal isolated from external analog and digital signals
Timer		-	
Digital I/O		2ch opto-isolated input (Negative logic) 4ch opto-isolated Open Collector Output (Negative logic)	2 opto-isolated inpout, 4 opto-isolated output (Negative logic)
Interrupt	**Interrupt Request Causes**	External trigger / Transformation end	External trigger or A/D Transformation end
	Interrupt Request Level	One of IRQ 3~7, 9~12, 14 or 15	
I/O address		Any 4-byte boundary	
Power consumption (Max.)		+5VDC 500mA	+5VDC 850mA
Bus / Dimension (mm)		AT Bus / 163.0(L) x 122.0(H)	
Connector		37-pin female D-type	
Option	**Software**	ACX-PAC(W32)BP, ACX-PAC(W32)AP, API-PAC(W32), SUPPORT-PAC(PC)302, DDE SERVER(W32)	ACX-PAC(W32)BP, ACX-PAC(W32)AP, API-PAC(W32), SUPPORT-PAC(PC)303, DDE SERVER(W32)
	Accessories	DTP-3(PC), DTP-4(PC), EPD-37 *2	
	Cables / Connector	PCA37P, PCB37P, PCA37PS, PCB37PS	
CE marking		-	○

*1: Conversion Accuracy: A value in the table is linearity error at 25℃ .
*2: Option cable of PCB37P or PCB37PS is required.