High Speed Bi-Directional Digital I/O

PIO-32DM(PCI)

PCI Bi-direct 32 Bus Master CE

Includes API-PAC(W32) [API Function Library]

FEATURES

★ 32 signals(configurable as 32 input signals, 16 I/O signalsor 32 output signals)
★ Stores digital signals at a sampling rate of 20 MHz - capable of detecting patterns (pattern input) and being used as a 20 MHz digital pattern generator (pattern output)
● Equipped with sync signal control connection. By using a sync signal cable, multiple boards can operate in sync with one another.

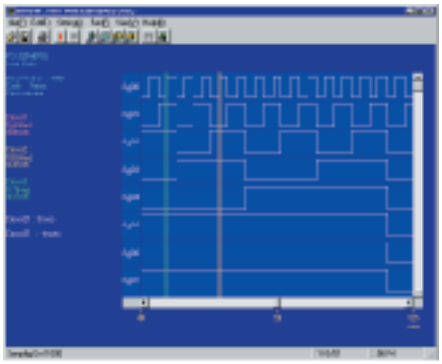
SPECIFICATIONS

No. of channels	32ch (16 input signals, 16 I/O signals or 32 output signals - software selectable)
I/O Circuit	
Signal level	5VDC
Internal power	-
Input specifications	
Type	TTL level (positive logic)
Interrupt	Errors and various factors, one interrupt request line as INTA
Resistance	10kΩ
Output specifications	
Type	TTL level (positive logic)
Rating	5VDC 24mA
Response time	50nsec (max)

Wiring distance	1.5m (max)
I/O address	Occupies 2 : any 32- and 64-byte boundary
Power consumption	5VDC 700mA (max)
PCI bus /	32bit, 33MHz, 5V /
Dimensions (mm)	176.41(L) x 106.68(H)
Connector	Sync. Section: PS-10PE-D4L1-B1 [JAE] or equivalent x 2 Digital Section: PCR-96LMD [HONDA] or equivalent
Options	
Software	-
Accessories	EPD-96 *1, DTP-64 *1
Cables / Connectors	PCA96PS-0.5/1.5, PCB96PS-0.5/1.5, PCA96P-1.5, PCB96P-1.5, CN5-H96F

*1: Requires use of optional cable PCB96P or PCB96PS

Functions



Bus Master Transfer
Utilizing Bus Master, the data transfer rate between the PC and CONTEC's PIO-32/32DM(PC)board is 80MB / sec. (133MB /sec. -max) without putting additional burden on the host computer's CPU.

Synchronization Control
The board is equipped with a synchronization control connector to allow easy inter-board synchronization.
16 boards (max) can be interconnected (including the master).

Pattern Input / Output
The PIO-32DM(PCI) stores digital signals at a sampling rate of 20 MHz and is capable of detecting patterns (pattern input). It can also be used as a 20 MHz digital pattern generator (pattern output).

Parallel Port Extension

PRN-2(PCI)

PCI CE

Includes LPT Driver for Windows© XP/NT/Me/98/95

FEATURES

★ Provides 2 interface channels IEEE 1284 / Centronics - compatible
★ Supports 5 modes of IEEE-1284 (Compatibility, Nibble, Byte, EPP, ECP) user selectable dependant on OS and device
● Under Windows NT 4.0 up to 16 PRN-2(PCI) boards can be used at the same time
Only one board can be used under Windows XP/2000/Me/98/95

SPECIFICATIONS

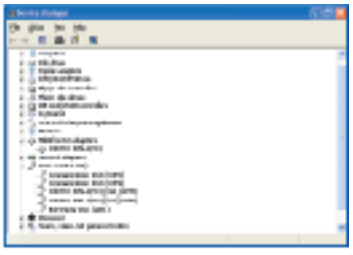
Channels	2ch
Communication standard	IEEE-1284
Communication mode	Compatibility, Nibble, Byte, EPP, ECP
Hardware Specifications	TTL level (+5VDC)
Controller	ST78C236CJ44(EXAR) or equivalent
Reference clock	24MHz (Reference clock of on-chip LSI)
Wiring distance	5m max
Interrupts	1 level
I/O address	16 ports (control port) + (8 ports + 4 ports) x 2
Supply voltage	5VDC (±5%)
Power consumption	5VDC 150mA (max)
Dimensions (mm)	121.69(L) x 106.68(H)

Connector	DHA-RP36-R13AN [DDK] or equivalent
Options	
Software	-
Accessories	-
Cables / Connector	PRN-CB105

PRN-CB105 (5m)

Support Software

PRN-DRV(W32)
LPT driver software for PRN-2(PCI)



PRN-2(PCI) drivers for Windows© PRN-DRV(W32) is a utility software that displays the LPT numbers assigned to the individual channels on CONTEC's PRN-2(PCI).

For latest driver versions, please visit our web site.

* Transfer mode is dependant on device type and operating system (refer to the following table,)

Operating System	IEEE 1284				
	Compatibility	Nibble	Byte	EEP	ECP
Windows XP/2000/Me/98/95	○	○	○	○	○
Windows NT 4.0	○	○			