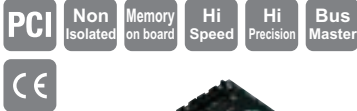


High Speed Multi-Function A/D

ADA16-32/2(PCI)F



API-PAC(W32) included [API function library]

ATP-32F
BNC Terminal Unit for Analog I/O (32ch)



ATP-8
BNC Terminal Unit for Analog I/O (8ch)



FEATURES

- Analog input/output, digital input/output and counter functions
- Event Controller for diverse sampling control
- Bus Master Transfer alleviates the load on host computer's CPU
- 64k data buffer memory enables background processing

SPECIFICATIONS

Analog Input	
Channels	32 single-ended, 16 differential
Range	Bipolar: $\pm 10V$, $\pm 5V$, $\pm 2.5V$ or Unipolar: $0\sim+10V$, $0\sim+5V$, $0\sim+2.5V$
Impedance	1M Ω or more
Resolution	16bit
Conversion Speed	2 μ sec/ch (Max.)
Nonlinearity error *2	$\pm 5LSB$
Buffer memory	64K-word FIFO or 64K-word RING
Analog Output	
Channels	2
Range	Bipolar: $\pm 10V$, $\pm 5V$, $\pm 2.5V$, $\pm 1.25V$ or Unipolar: $0\sim+10V$, $0\sim+5V$, $0\sim+2.5V$
Resolution	16bit
Conversion Speed	10 μ sec/ch (max)
Nonlinearity error *2	$\pm 3LSB$
Buffer memory	64K-word FIFO or RING

Digital I/O	
Input	8 TTL-level (positive logic)
Output	8 TTL-level (positive logic)
Counter	
Channels	2
Counting system	32-bit Up count
Max. count	32-bit (binary data)
Interrupts	1 interrupt
I/O address	4 ports x 1, 256 ports x 1 occupation
Power Consumption	+5VDC 1100mA (max)
Connector	PCR-96LMD [HONDA Tsushin Kogyo]
PCI Bus / Dimensions (mm)	32bit, 33MHz, 5V or 3.3V*1 / 176.41(L) x 105.68(H)
Options	
Software	-
Accessories	DTP-64, EPD-96, ATP-8, ATP-32F
Cables / Connectors	PCA96PS-0.5/1.5, PCB96PS-0.5/1.5, PCA96P-1.5, PCB96P-1.5, CN5-H96F

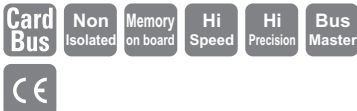
*1: +5V power must be supplied from PCI bus slot.
*2: When using a signal source with a high-speed built-in operational amplifier.

C-07

ANALOG I/O

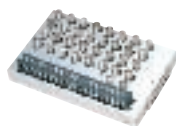
High Speed 16-Bit Multi-Function A/D

ADA16-32/2(CB)F



API-PAC(W32) included [API function library]

ATP-32F
BNC Terminal Unit for Analog I/O (32ch)



ATP-8
BNC Terminal Unit for Analog I/O (8ch)



FEATURES

- Analog input/output, digital input/output and counter functions
- Event Controller for diverse sampling control
- Bus Master Transfer alleviates the load on host computer's CPU
- 64k data buffer memory enables background processing

SPECIFICATIONS

Analog Input	
Channels	32 single-ended, 16 differential
Range	Bipolar: $\pm 10V$
Impedance	1M Ω or more
Resolution	16bit
Conversion Speed	2 μ sec/ch (Max.)
Nonlinearity error *2	$\pm 5LSB$
Buffer memory	64K-word FIFO or 64K-word RING
Analog Output	
Channels	2
Range	Bipolar: $\pm 10V$
Resolution	16bit
Conversion Speed	10 μ sec/ch (Max.)
Nonlinearity error *2	$\pm 3LSB$
Buffer memory	64K-word FIFO or 64K-word RING
Digital I/O	
Input	4 TTL-level (positive logic)
Output	4 TTL-level (positive logic)

Counter	
Channels	1
Counting system	32-bit Up count
Max. count	32-bit (binary data)
Interrupts	1 interrupt
I/O address	4 ports x 1, 256 ports x 1 occupation
Power Consumption	+3.3VDC 600mA (max)
Operating condition	0~50°C, 10~90% RH (no condensation)
Length of Attached Cable	0.5m
Correspondent card slot	CardBus of PC Card Standard
Weight	200g (including PC Card and cables)
Options	
Software	-
Accessories	DTP-64, EPD-96, ATP-8, ATP-32F
Cables / Connectors	-

*1: When using a signal source with a high-speed built-in operational amplifier.

Product Lineup
/ Basic
Knowledge

F Series
Multi-Function I/O

Features

PCI

PC

E Series
Intelligent I/O

Features

PCI

ISA

Standard I/O

Low Profile
PCI /
PCI

PC Card

USB

ISA