

E Series Features

1. Bulk buffer memory

Data bulk buffer memory (FIFO or ring buffer) capable of storing up to 262,144 bits of data, enabling high speed sampling to be executed independently of the processing power of the PC. Either FIFO or ring format can be selected as memory type.

2. Diverse sampling control

Sampling start / stop can be controlled via software, by using the signal change of specified channels or by utilizing external digital signals as a trigger. Consecutive samplings can be synchronized with the onboard timer or with external pulse signals.

3. Interrupt events

Interrupt events can be generated by factors such as sampling termination, changes in external signal or sampling errors allowing board status to be monitored with no additional load on the host computer,

4. Analog output

Independent 1-channel analog output (digital to analog conversion)

5. Digital input / output

4 points of TTL level digital input and 4 points of digital output

6. Others

PIT (i8254 equivalent)

Dedicated function upgrades

A variety of functions can be added by using available add-on function boards

● Channel expansion

Allows an analogue E Series 16 channel single-ended / 8 channel differential board to provide 32 single-ended channels / 16 differential channels

● Insulation Amplifier

Provides both bus and channel-to-channel insulation

● Concurrent sampling

Allows 16 channels to be sampled in the same timing

● Low Pass Filter

Can lower commercial power frequency and provide wide area filtering (antialiasing)

A/D Board Accessories & Cables

