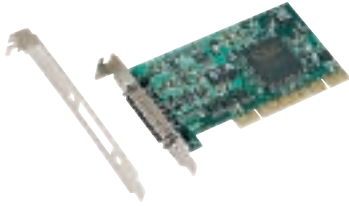


[ANALOG I/O | Standard I/O] Low Profile PCI / PCI

For options, please see Page J-08 (Accessories & Cables) and Page I-01 (Software)
Please see Page 2-1 for PCI specifications

Low Profile PCI / PCI
Low-Cost 16-Bit Multi-Function A/D
ADA16-8/2(LPCI)L



API-PAC(W32) Included [API Function Library]

BNC Terminal Unit for Analog I/O (8ch)
ATP-8L



* Please visit our website for more details.

SPECIFICATIONS

Analog Input	
Number of channels	8 single-ended
Range	Bipolar: $\pm 10V$
Impedance	1M Ω or more
Resolution	16bit
Nonlinearity error ^{*3}	$\pm 5LSB$
Conversion Speed	10 μ sec/ch (max)
Buffer memory	1k Word
Analog Output	
Channels	2
Range	Bipolar: $\pm 10V$
Impedance	1 Ω or less
Resolution	16bit
Nonlinearity error	$\pm 5LSB$
Conversion Speed	10 μ sec/ch (max)
Buffer memory	1k Word

Digital I/O	
Input channels	4 TTL (positive logic)
Output channels	4 TTL (positive logic)
Counter	
Number of channels	1
Counting System	32-bit Up counter
Max. Counts	32-bit binary data
Interrupts	One interrupt request signal as INTA
I/O address	Any 64-byte boundary
Power Consumption	5VDC 380mA (max)
Connector	10250-52A2JL[3M] or equivalent
PCI Bus /	32bit, 33MHz, 5V or 3.3V ^{*2} /
Dimensions (mm)	121.69(L) x 63.41(H)
Options	
Software	-
Accessories	EPD-50A ^{*1} , ATP-8L ^{*1}
Cables / Connectors	PCB50PS-0.5P/1.5P, PCA50PS-0.5P/1.5P

*1: Requires use of optional cable PCB50PS-0.5P/1.5P
*2: +5V power must be supplied from PCI bus slot.
*3: When using a signal source with a high-speed built-in operational amplifier.

FEATURES

- Low Profile PCI - compliant (includes bracket for use in standard PCI slot.)
- Provides high-precision analog input / output, digital input / output and counter functions
- On-board control mechanism provides analog input / output, timed input / output and input/output that is synchronized with external signals
- 1k data buffer allows background processing
- Features software for analog input / output correction

Standard PCI/Low Profile PCI
Low-Cost Analog to Digital Input
AD16-16(LPCI)L



API-PAC(W32) Included [API Function Library]

BNC Terminal Unit for Analog I/O (8ch)
ATP-8L



* Please visit our website for more details.

SPECIFICATIONS

Analog Input	
Channels	8 single-ended
Range	Bipolar: $\pm 10V$
Impedance	1M Ω or more
Resolution	16bit
Nonlinearity error ^{*4}	$\pm 5LSB$
Conversion speed	10 μ sec/ch (max)
Buffer memory	1k Word
Digital I/O	
Input channels	4 TTL (positive logic)
Output channels	4 TTL (positive logic)
Counter	
Number of channels	1
Counting System	32-bit Up counter
Max. Count	32-bit binary data

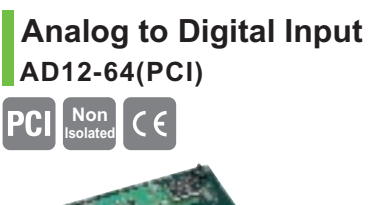
Interrupts	One interrupt request signal as INTA
I/O address	Any 64-byte boundary
Power Consumption	5VDC 260mA (max)
Connector	10250-52A2JL[3M] or equivalent
PCI Bus /	32bit, 33MHz, 5V or 3.3V ^{*2} /
Dimensions (mm)	121.69(L) x 63.41(H)
Options	
Software	-
Accessories	EPD-50A ^{*1} , ATP-8L ^{*1+3}
Cables / Connectors	PCB50PS-0.5P/1.5P, PCA50PS-0.5P/1.5P

*1: Requires use of optional cable PCB50PS-0.5P/1.5P
*2: +5V power must be supplied from PCI bus slot.
*3: Maximum analog input channels = 8.
*4: When using a signal source with a high-speed built-in operational amplifier.

FEATURES

- Low Profile PCI - compliant (includes bracket for use in standard PCI slot.)
- Provides high-precision analog input / output, digital input / output and counter functions
- On-board control mechanism provides analog input / output, timed input / output and input/output that is synchronized with external signals
- 1k data buffer allows background processing
- Features software for analog input / output correction

Analog to Digital Input
AD12-16(PCI)



API-PAC(W32) Included [API Function Library]

SPECIFICATIONS

Input channels	[AD12-16(PCI)]
	16 single-ended, 8 differential
Resolution	[AD12-64(PCI)]
	64 single-ended, 32 differential
Input specifications	
Range	$\pm 10V, \pm 5V, \pm 2.5V, \pm 1.25V$ 0~+10V, 0~+5V, 0~+2.5V, 0~+1.25V (channels individually selectable via software)
Gain	-
Conversion speed ^{*1}	10 μ sec/ch (Max.)
Nonlinearity error ^{*2}	$\pm 10V, \pm 5V, 0\sim+10V, 0\sim+5V: \pm 2LSB$
	$\pm 2.5V, \pm 1.25V, 0\sim+2.5V: \pm 4LSB$ 0~+1.25V: $\pm 8LSB$
Impedance	1M Ω or more
Trigger	1 TTL level input
Isolation	-
Timer	0.5 μ sec~17min (selectable in step of 250nsec)
Digital I/O	General Digital I/O: Input 3, Output 4 (TTL-level)

Interrupts	
Request Events	8 modes
Request Levels	One interrupt request signal as INTA
I/O address	Any 32-byte boundary
Power Consumption	5VDC 700mA (max)
Connector	PCR-E96LMD [HONDA Tsushin Kogyo] or equivalent
PCI Bus /	32bit, 33MHz, 5V /
Dimensions (mm)	176.41(L) x 106.68(H)
Options	
Software	ACX-PAC(W32)BP
Accessories	EPD-96 ^{*3}
Cables / Connectors	PCA96PS-0.5/1.5, PCB96PS-0.5/1.5, PCA96P-1.5, PCB96P-1.5, CN5-H96F

*1: Actual conversion speed is dependent upon operating system software.
*2: When using a signal source with a high-speed built-in operational amplifier.
*3: Requires use of optional cable PCB96P or PCB96PS

FEATURES

- AD12-64(PCI) supports a multi-channel environment via 64 single-ended inputs
- Sampling Control function enables data input via onboard program timer or an external clock
- Independent programmable timer and TTL-level external trigger
- 4 TTL-level digital inputs, 4 TTL-level digital outputs

Analog to Digital Input
AD12-64(PCI)



API-PAC(W32) Included [API Function Library]