
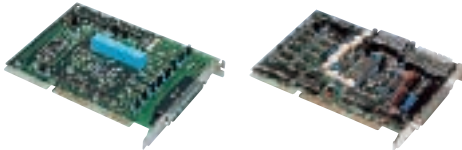


ISA	Model	8 Single-Ended Analog to Digital Input Board	Analog to Digital Input Board	Opto-Isolated Analog to Digital Input Board
		AD12-8LT(PC)	AD12-16(PC)	AD12-16LG(PC)
				
SPECIFICATIONS				
Input channels		8 single-ended input	16 single-ended or 8 differential input	16 single-ended or 8 differential input
Resolution		12bit		
Input specification	Input range	±5V	±5V, 0~5V, 0~10V	±5V
	Input gain	-		x1, x2, x4, x8, x16 (software selectable)
	Conversion speed	25 μsec/ch	20 μsec/ch	15 μsec/ch
	Conversion Accuracy*1	±3LSB	±2LSB	±3LSB
Input Impedance		100MΩ or more	1MΩ or more	100MΩ or more
Trigger		1 TTL-level input		
Isolation		-		
Timer		16-bit down counter × 3 (82C54 or equivalent)	2~7 × 10 ¹³ μsec	
Digital I/O		3 TTL-level input, 4 CMOS-level output (Negative logic)	1 TTL-level input/output (Negative logic)	8 TTL-level input/output (Positive logic)
Interrupt	Interrupt Request Events	External trigger	External trigger / Timer / Conversion end	Conversion end
	Interrupt Request Levels	One of IRQ 3~7, 9		
I/O address		Any 8-byte boundary	Any 16-byte boundary	Any 8-byte boundary
Power consumption (Max.)		+5VDC 400mA, +12VDC 15mA	+5VDC 700mA	+5VDC 400mA, +12VDC 25mA
Bus / Dimension (mm)		XT Bus/130.0(l)×107.0(h) (5.12"×4.21")		
Connector		37-pin male D-type		
Option		ACX-PAC(W32)BP, API-PAC(W32)		
Option	Accessories	DTP-3(PC) *2, DTP-4(PC) *2, EPD-37A *2, EPD-37 *2	DTP-3(PC), DTP-4(PC), EPD-37A *3, EPD-37 *3	DTP-3(PC) *2, DTP-4(PC) *2, EPD-37A *2, EPD-37 *2
	Cables / Connector	DT-6, DT-11		
	Connector	PCA37P, PCB37P, PCA37PS, PCB37PS		
CE marking		N	Y	Y

*1: Conversion Accuracy: Operating at 25°C

*2: Requires use of optional cable DT-6

*3: Requires use of optional cable PCB37P or PCB37PS

ISA	Model	Isolated Analog to Digital Input Board	Opto-Isolated Analog to Digital Input Board
		ADI12-8CL(PC)H	ADI12-16(PC)
			
SPECIFICATIONS			
Input channels		8 single-ended input	16 single-ended or 8 differential input
Resolution		12bit	
Input specification	Input range	0~5V, 1~5V, 0~20mA, 4~20mA	±5V, ±10V, 0~10V, 4~20mA
	Input gain	-	
	Conversion speed	1200 μsec/ch	25 μsec/ch
	Conversion Accuracy*1	±3LSB	
Input Impedance		1MΩ or more	
Trigger		1 opto-isolated input (share 1 of digital input)	1 opto-isolated input (shared signal of Rising-edge or digital input)
Isolation		Individual isolation	Bus isolation
Timer		-	
Digital I/O		2ch opto-isolated input (Negative logic) 4ch opto-isolated Open Collector Output (Negative logic)	2 opto-isolated input, 4 opto-isolated output (Negative logic)
Interrupt	Interrupt Request Causes	External trigger / Conversion end	
	Interrupt Request Level	One of IRQ 3~7, 9~12, 14 or 15	
I/O address		Any 4-byte boundary	
Power consumption (Max.)		+5VDC 500mA	+5VDC 850mA
Bus / Dimension (mm)		AT Bus / 163.0(L) × 122.0(H) (6.42" × 4.8")	
Connector		37-pin female D-type	
Option		ACX-PAC(W32)BP, API-PAC(W32)	
Option	Accessories	DTP-3(PC), DTP-4(PC), EPD-37A *2, EPD-37 *2	
	Cables / Connector	PCA37P, PCB37P, PCA37PS, PCB37PS	
	Connector	PCA37P, PCB37P, PCA37PS, PCB37PS	
CE marking		N	Y

*1: Conversion Accuracy: Operating at 25°C

*2: Requires use of optional cable PCB37P or PCB37PS